

Low cost IBC cell processes for industrial application

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➤ PV industry facts and trends

- n-type Si cell technologies
- Industrial fabrication processes

➤ Low cost IBC approach in HERCULES (ZEBRA cell concept)

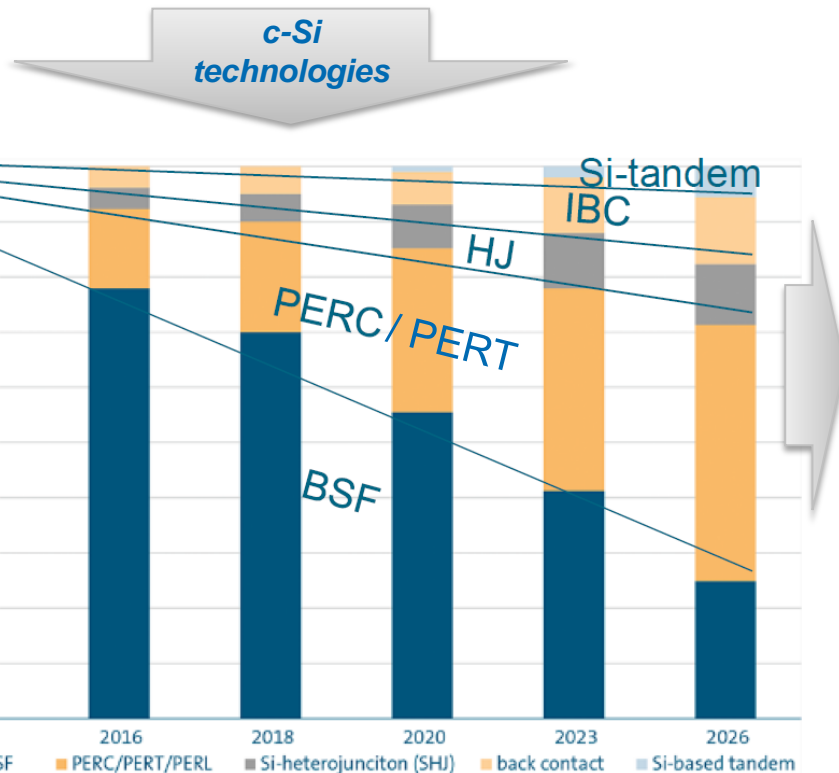
- Main process steps
 - *Diffusion and passivation*
 - *Patterning method*
 - *Metallization and interconnection*
- Best cell and pilot line results
- Today's technology main limitation
- Potential for further improvements

➤ Conclusions

PV industry facts and trends

Worldwide market share for different solar cell technologies (end of 2015):

- ~90% c-Si (mono-Si, multi-Si)
- ~10% Thin film (Cd-Te, a-Si, CIGS)

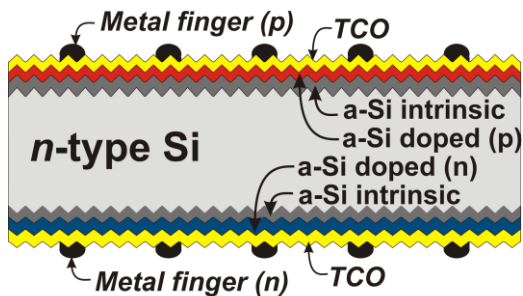


- **AI-BSF:**
 - *p-type* mono or multi wafers
 - low complexity, few process steps
- **PERC:**
 - *p-type* mono wafers
- **PERT:**
 - *p-type* mono and multi wafers
 - *n-type* mono wafers
- **HJ:**
 - *n-type* mono wafers
- **IBC:**
 - *n-type* mono wafers

PV market dominated by p-type cell concepts !

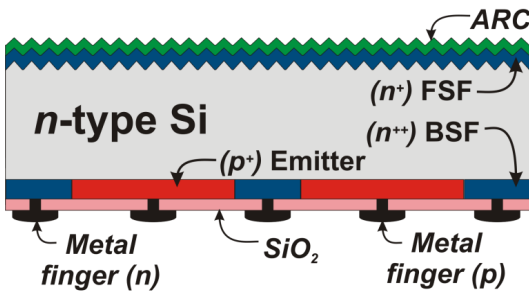
Data from: International Technology Roadmap for PV, 2016

Most efficient c-Si cells commercially produced (R&D results):



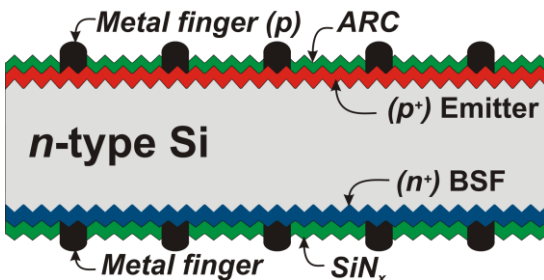
HJ solar cell

- front, emitter: a-Si (p)
- back, BSF: a-Si (n)
- bifacial
- $\eta \leq 25.6\%$ (Panasonic)



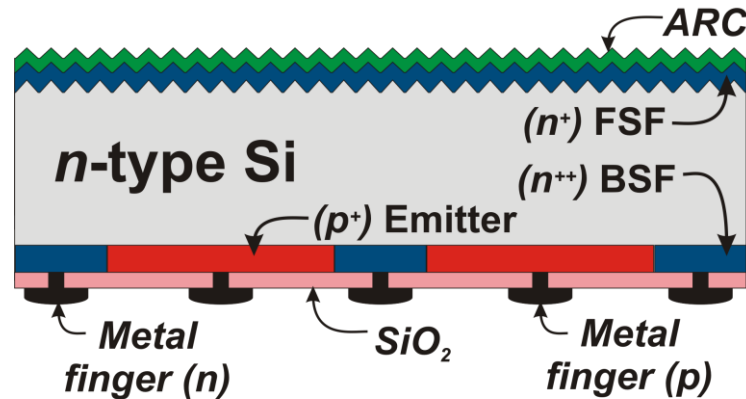
IBC solar cell

- FSF/BSF: n^+/n^{++} diffusion
- emitter: p^+ diffusion
- $\eta \leq 25.1\%$ (Sunpower)



n-PERT solar cell

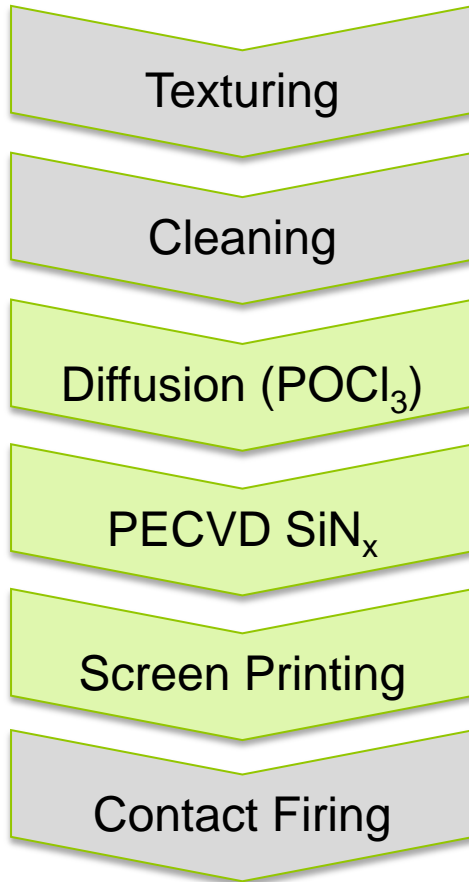
- front, emitter: p^+ diffusion
- back, BSF: n^+ diffusion
- bifacial
- $\eta \leq 22\%$ (LG)



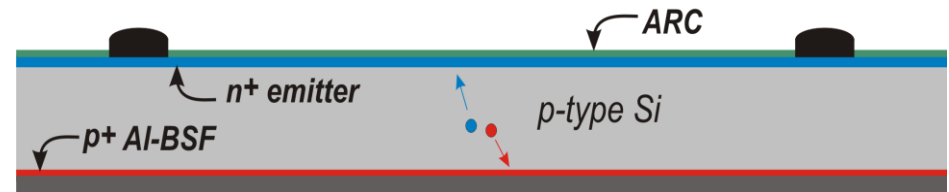
Requirements to make IBC cells in a standard production line:

- *large pitch size (for screen printed metallization)*
- *few masking steps*
- *low cost patterning method*
- *simple and reliable finger interconnection method (BB)*

Processes / steps



Used in >70% of PV manufacturing
(Al-BSF concept)

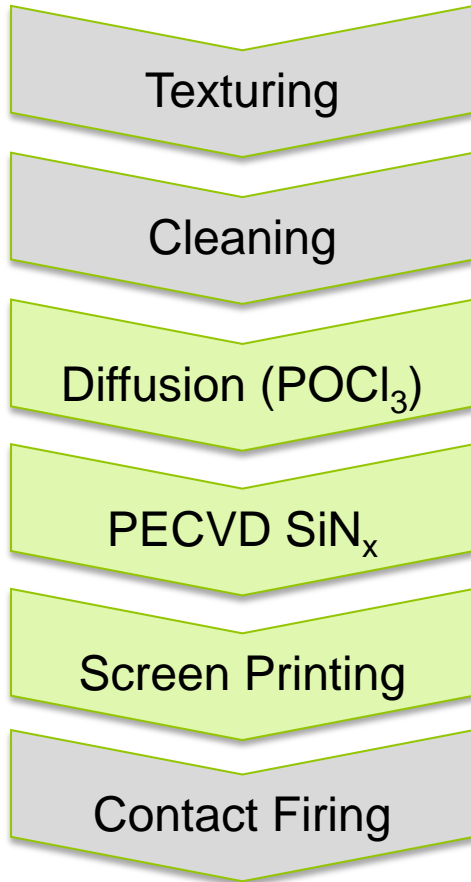


- efficiency: 18.5% – 20.0%
- full Al back side contact (monofacial cell)
- high back side recombination
- p-type Cz wafers $156 \times 156 \text{ mm}^2$ (6-inch)

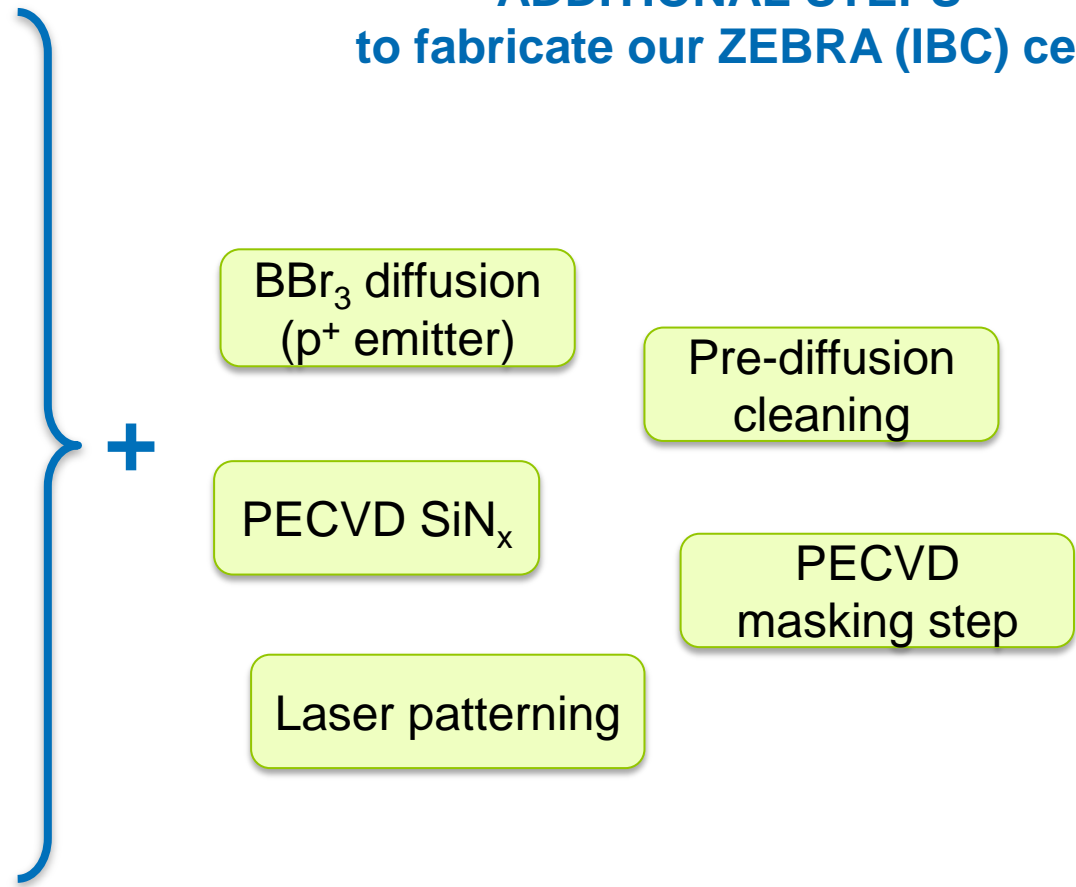
HERCULES project: develop low cost and high efficiency IBC cells

HERCULES: Low cost IBC approach

Processes / steps



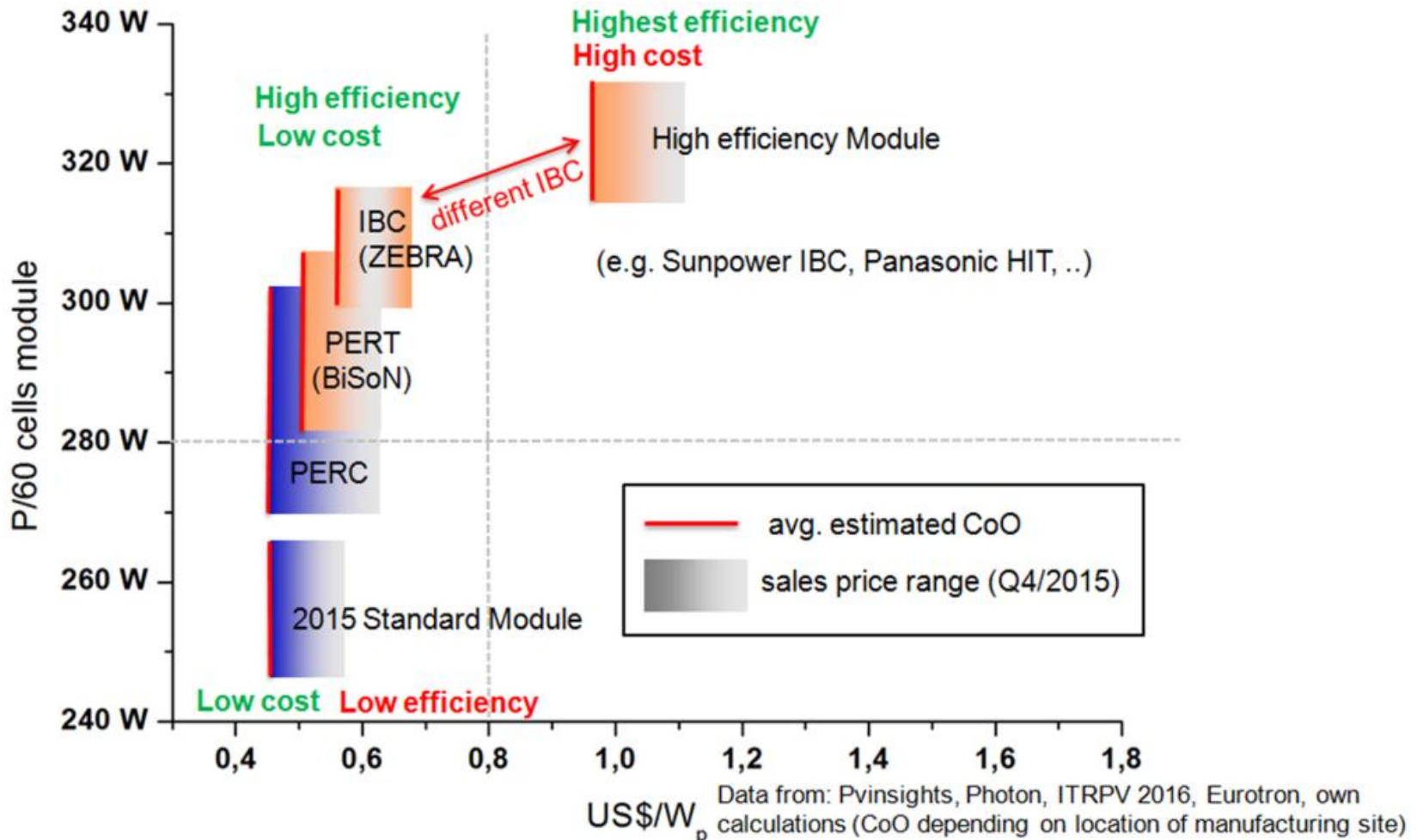
ADDITIONAL STEPS to fabricate our ZEBRA (IBC) cell



HERCULES: Low cost IBC approach



International Solar Energy
Research Center Konstanz



R. Kopecek *et al.*, Photovoltaics International Volume 30, May (2016)



ZEBRA (IBC) cell: basic features

- **large area:**

industry-standard $156 \times 156 \text{ mm}^2$
pseudo square *n*-type Cz wafers

- **screen printed metallization:**

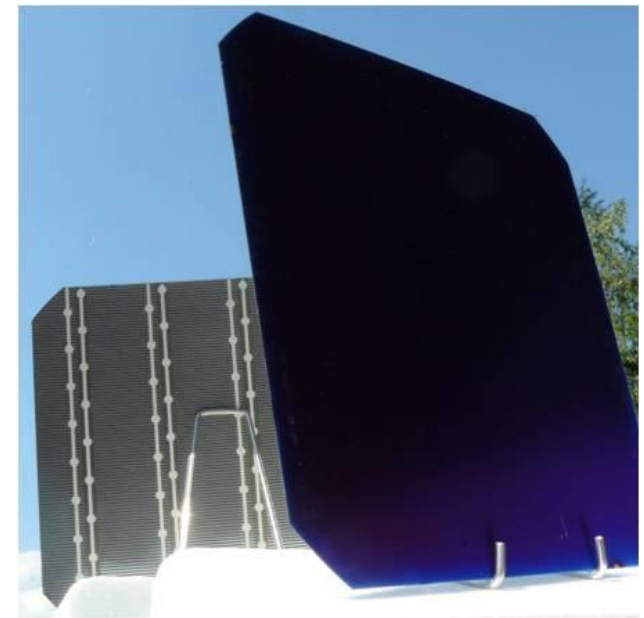
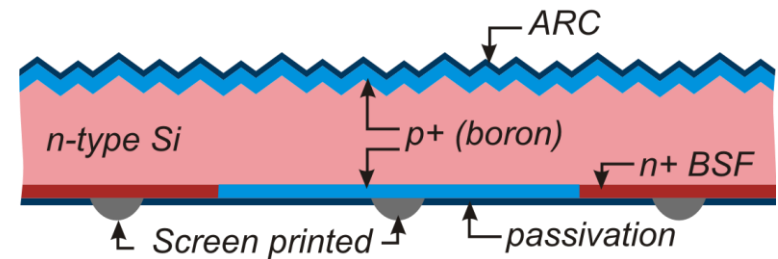
alignment tolerance: $\pm 150 \mu\text{m}$

- **variable busbar structure:**

a) conventional stringing methods
applicable for cell interconnection

⇒ bifacial: open back side grid

b) rear contact modules based on
conductive backsheet technology (as
used for MWT-cells & modules)

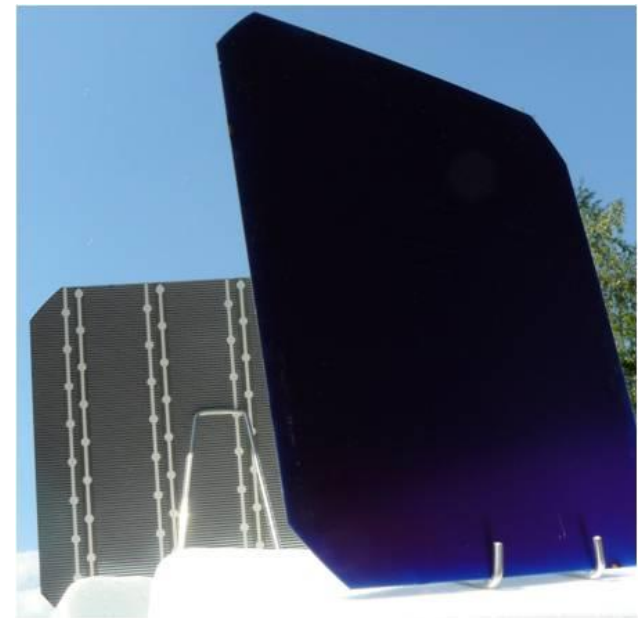
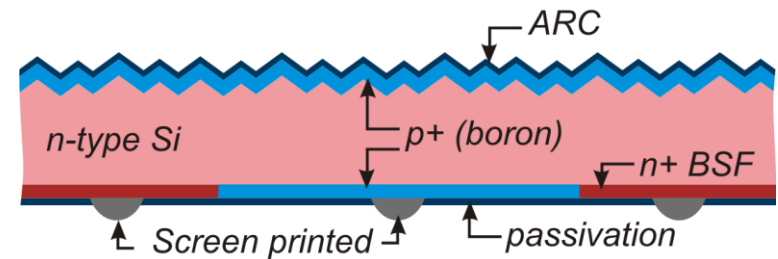


V.D. Mihailtchi *et al.*, **Patent pending:** WO2013087458A1
V.D. Mihailtchi *et al.*, Energy Procedia, 77, 534–539 (2015)

ZEBRA (IBC) cell: basic features

Main challenges for industrial implementation:

- p^+ (Boron) emitter and passivation
- patterning technique to form p^+ and n^+ regions
- p^+ and n^+ metallization
- finger interconnection



V.D. Mihailtchi *et al.*, **Patent pending:** WO2013087458A1
V.D. Mihailtchi *et al.*, Energy Procedia, 77, 534–539 (2015)

Boron diffusion and passivation



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Today's industrial techniques for boron doping:

1. BBr_3 thermal diffusion in an open tube furnace (~85%)
2. Ion implantation (~10%)
3. Other methods, e.g., APCVD of doped SiO_2 layers (~5%)



Boron diffusion and passivation

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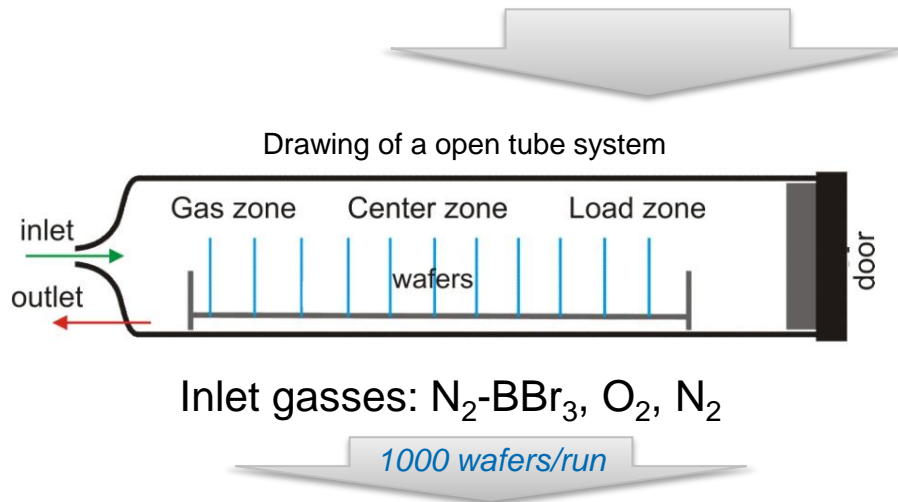
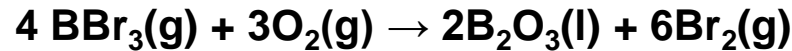


Photo of **centrotherm AG** diffusion system

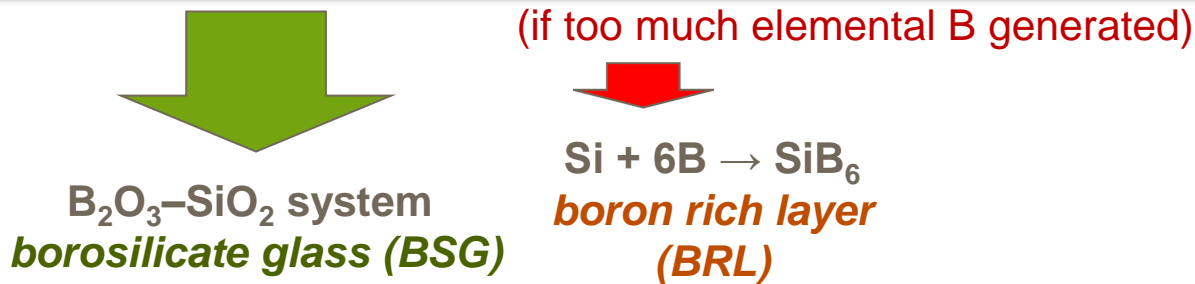
Boron diffusion and passivation



⇒ *deposition* (850-900 °C)



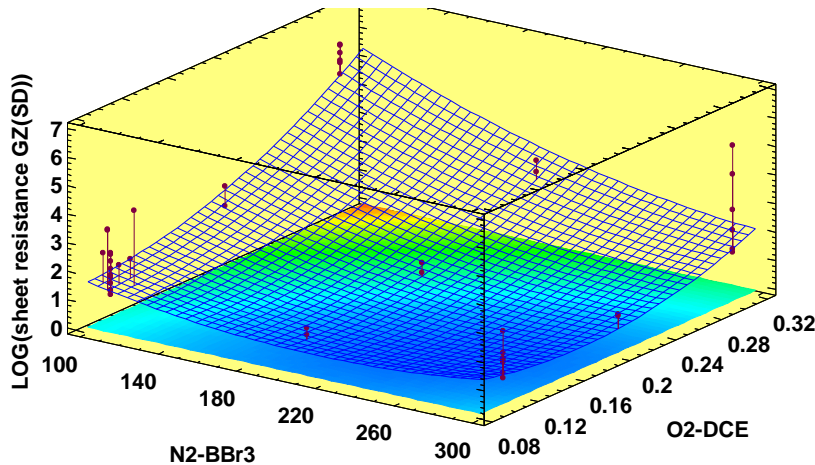
⇒ *drive in* (900-1050 °C)



A “careful” deposition is required for preventing BRL layer !

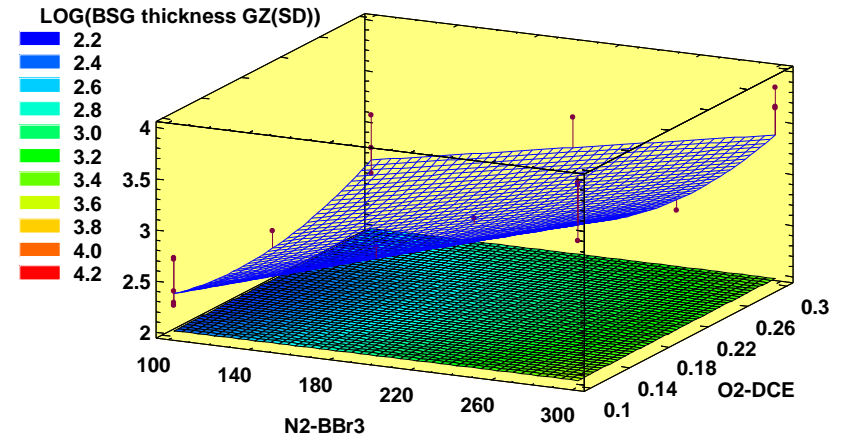
Results of typical diffusion optimization: *Multilevel factorial Design of Experiments (DoE)*

doping uniformity



Optimum sheet resistance uniformity
at: **high BBr3 flow and low O2 flow**

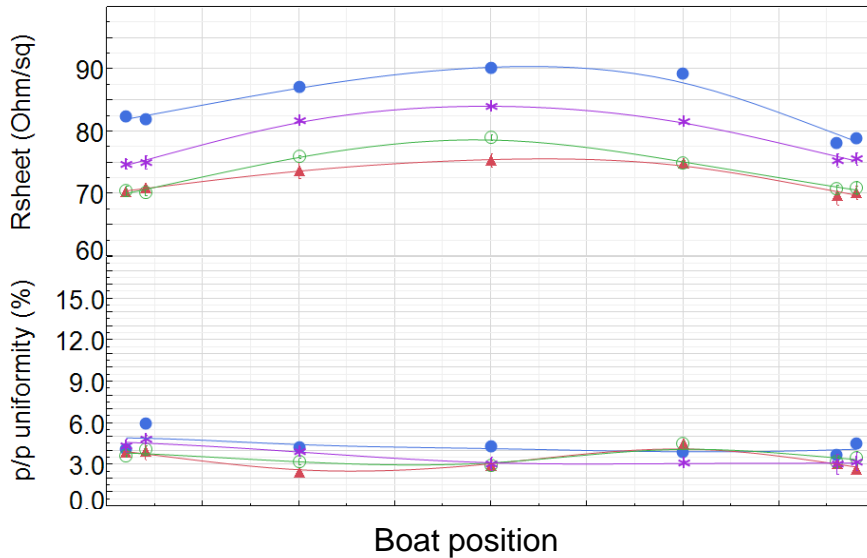
BSG thickness uniformity



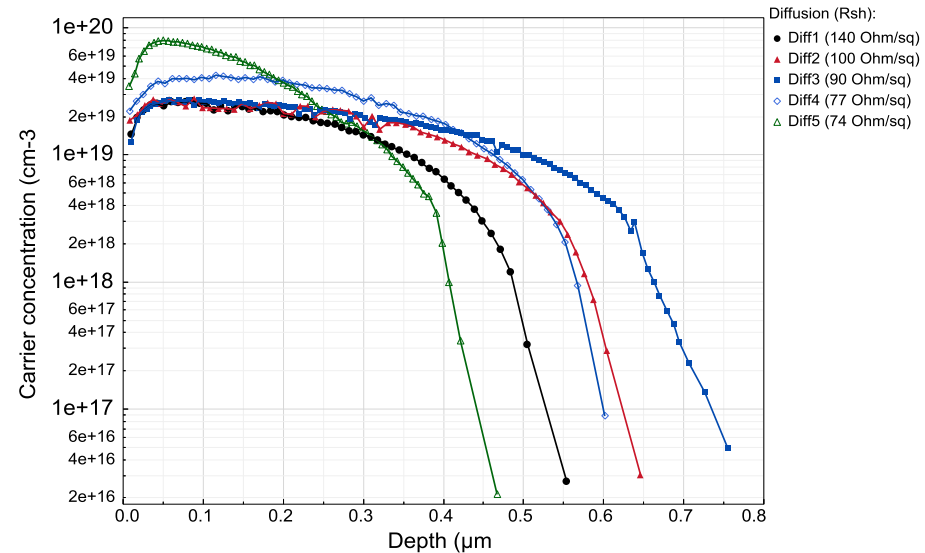
Optimum BSG thickness uniformity at:
low BBr3 flow and low O2 flow

Typical Boron diffusion results used in ZEBRA cell concept

Sheet resistance uniformity



Various Boron doping profiles

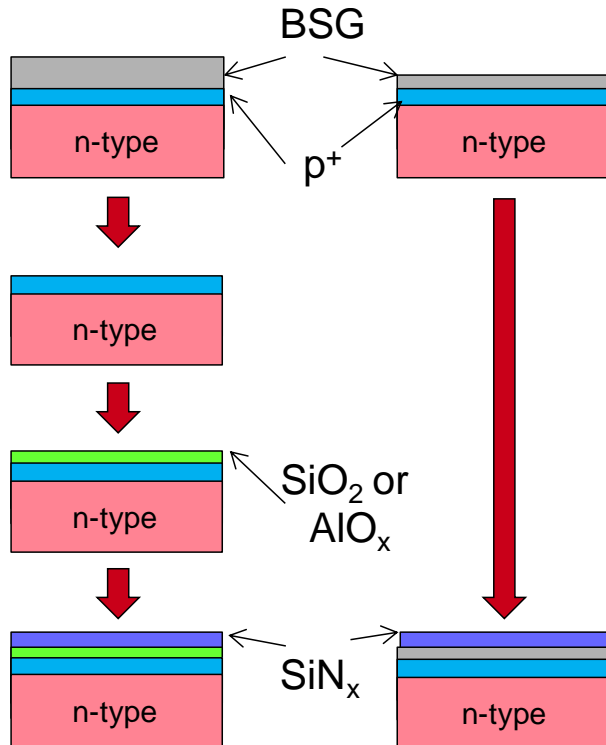


Boron diffusion and passivation

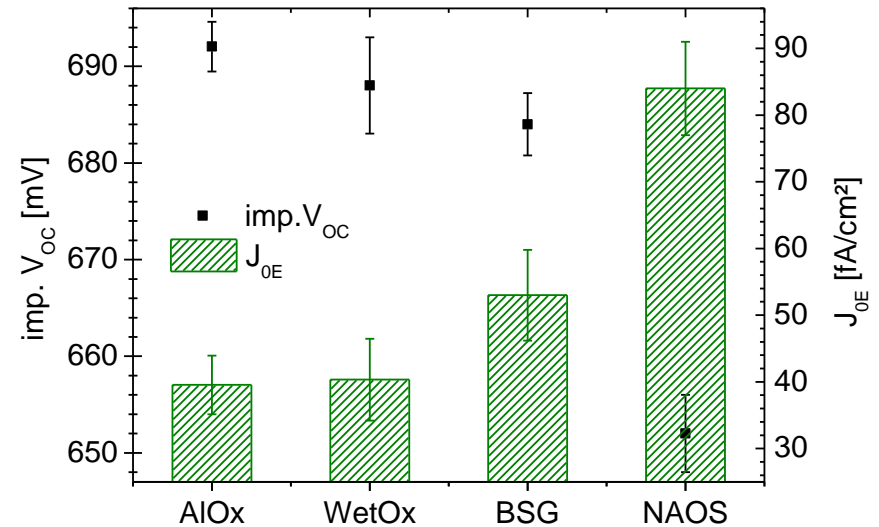
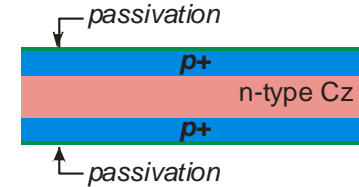
Boron emitter passivation: methods and results

Standard method

Our new method*



Test structure



AlO_x – PECVD AlO_x + SiN_x stack
 WetO_x – wet thermal SiO₂ (20 nm) + SiN_x stack
***BSG – in-situ boron glass BSG + SiN_x stack**
 NAOS – chemical SiO₂ (2-3 nm) + SiN_x stack

*V.D. Mihailetschi *et al.*, Patent pending: WO2011/160814A2

ZEBRA cell: patterning technique



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Options studied within HERCULES project:

- Laser patterning of a PECVD masking layer
- Interdigitated p^+ and n^+ patterning using a hybrid process (ion implant and diffusion) by counter-doping of masked implantation*

*R. Müller, J. Schrof, C. Reichel *et al.*, Appl. Phys. Lett. 105, 103503 (2014)



ZEBRA cell: patterning technique

Options studied within HERCULES project:

- Laser patterning of a PECVD masking layer
- Interdigitated p^+ and n^+ patterning using a hybrid process (ion implant and diffusion) by counter-doping of masked implantation*



Nanosecond green laser with a rectangular top hat spot profile of $300 \times 600 \mu\text{m}^2$.

Lab throughput (single source): 285 w/h.

Target industrial throughput (dual source): 1440 w/h.



$600 \times 300 \mu\text{m}^2$,
process line

Metallization (optimum process):

- screen printing and firing through,
- single printing step for p^+ and n^+ , using Ag paste \Rightarrow less printing steps, self alignment

optimum paste	$\rho_C (p^+)$ [$m\Omega cm^2$]*	$\rho_C (n^+)$ [$m\Omega cm^2$]*	R_L [Ω/cm]
paste 1 (Ag)	0.8 \pm 0.3	1.0 \pm 0.5	0.45
paste 2 (AgAl)	1.2 \pm 0.5	-	0.5

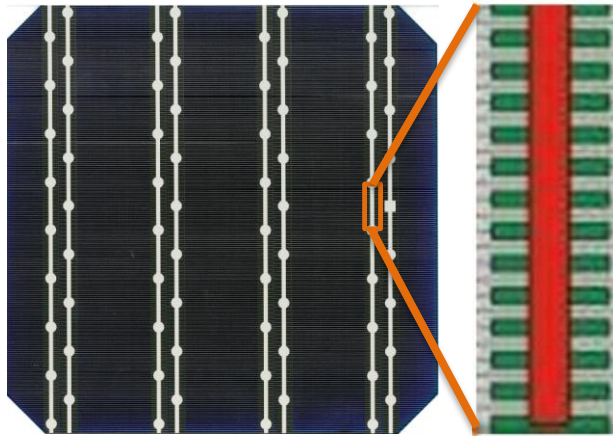
$$*R_{sh} (p^+) = 74 - 140 \Omega/sq$$

$$*R_{sh} (n^+) = 45 - 80 \Omega/sq$$

Interconnection:

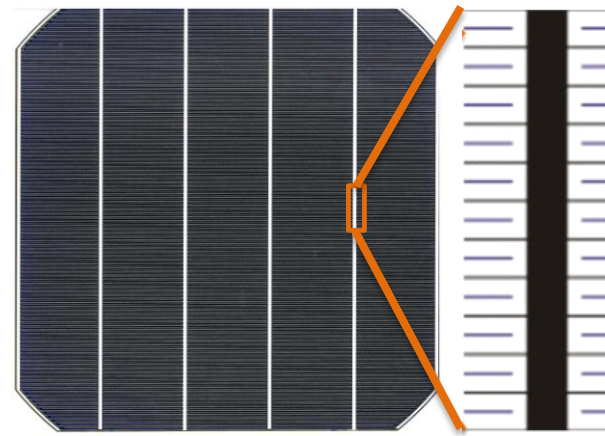
- Multi-layer (3D) vs. co-planar (2D) interconnection
- tested various layouts, busbar pastes, process parameters, etc.

Multi-layer interconnection (3D)



needs low temp. insulation pads
needs low temp. BB paste
flexible BB position

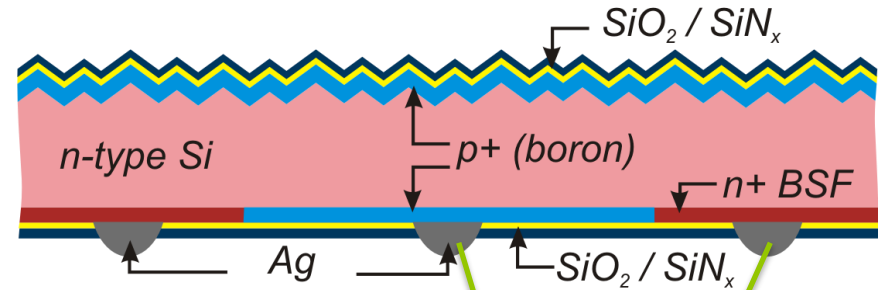
Co-planar interconnection (2D)



needs very accurate stringing
high temperature BB paste
edge BB needed

Interconnection (optimum process):

- Multi-layer (3D) interconnection

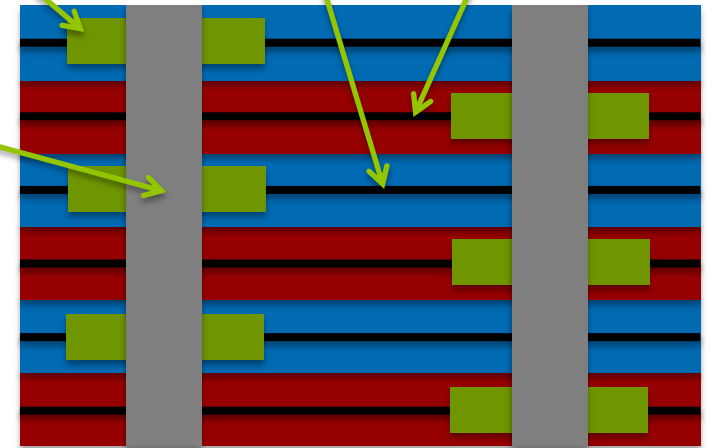


- Reliable and flexible design
- Compatible with wide range of low temp BB pastes
- TC and DH stable*



insulation pads

BB

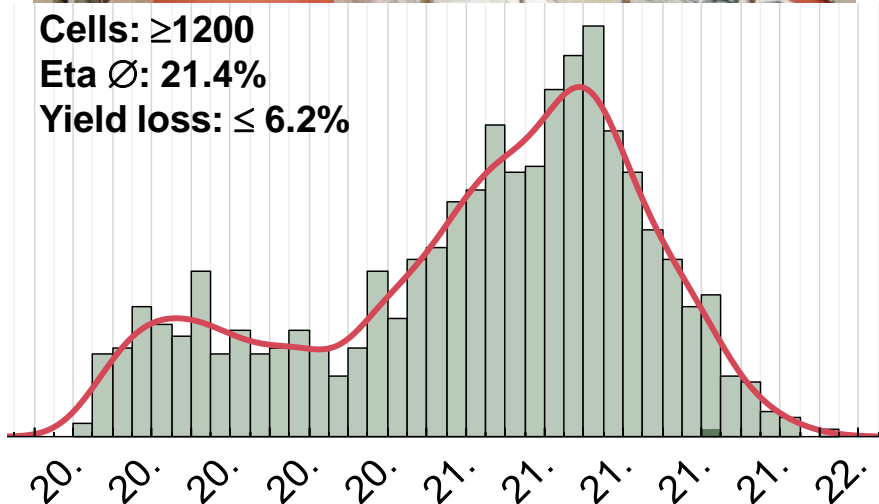


*A. Halm *et al.*, this workshop

ZEBRA: cell results



Cells: ≥ 1200
Eta $\bar{\varnothing}$: 21.4%
Yield loss: $\leq 6.2\%$



Pilot line activity of ZEBRA cells in HERCULES project

Main results:

- Ramp up and pilot production of >1200 n-type 6-inch IBC cells at ISC Konstanz
- $\geq 75\%$ bifaciality at cell level!
- best cell in pilot line 22%
- best 60 cells module power of 303 Wp*

Best cell results:

J_{SC} [mA/cm ²]	V_{OC} [mV]	FF [%]	Efficiency [%]
41.5	662	80.0	22.0

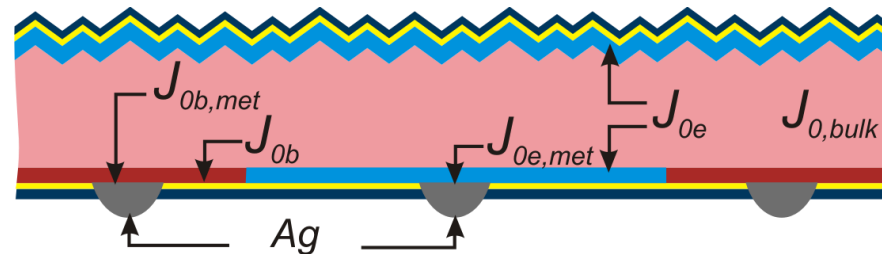
*A. Halm *et al.*, this workshop

ZEBRA: main limitation

	area [cm ²]	J_{SC} [mA/cm ²]	V_{OC} [mV]	iV_{OC} [mV]	FF [%]	Efficiency [%]
ZEBRA best cell	239	41.5	662	690	80.0	22.0

$$V_{OC} \ll iV_{OC}$$

$$V_{OC} = V_T \times \ln(J_{SC} / J_{01})$$



$$J_{01} = J_{0,bulk} + J_{ob} + J_{oe} + MF_e \times (J_{oe,met} - J_{oe}) + MF_b \times (J_{ob,met} - J_{ob})$$

From experimental data:

$$J_{ob} \approx 60 \text{ fA/cm}^2$$

$$J_{oe} \approx 25 \text{ fA/cm}^2$$

$$J_{ob,met} \approx 1200 \text{ fA/cm}^2$$

$$J_{oe,met} \approx 2500 \text{ fA/cm}^2 !$$

Reducing metal recombination losses by:

- tuning of (emitter) diffusion profiles
- improving metallization pastes (*actual state-of-the-art: Ag paste*)
- reduce metal contact area (*actual finger width: 50 μm*)

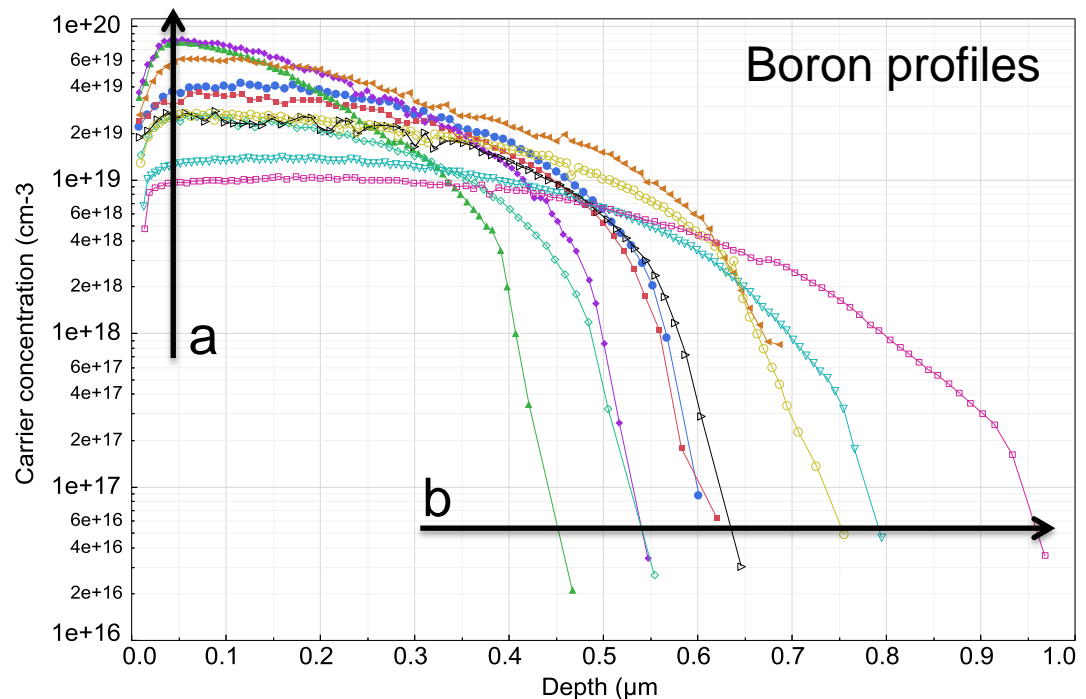
Design of Experiments (DoE)
with two factors:

a). Surface concentration, N_S

b). Junction depth, d_{pn}

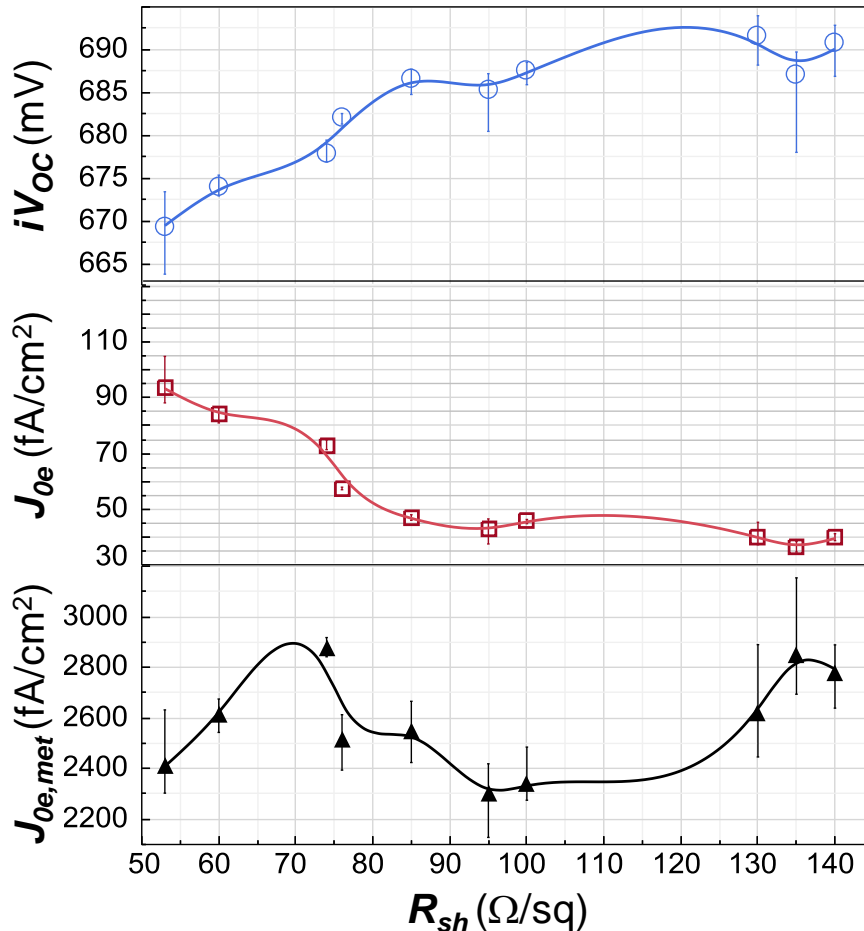
Response variables:

$$J_{0e,met}; iV_{OC}; V_{OC}; \text{Eta}; R_{sh}$$



ZEBRA: main limitation

iV_{OC} , J_{0e} and $J_{0e,met}$ as a function of boron emitter sheet resistance (R_{sh}):



Boron emitter:

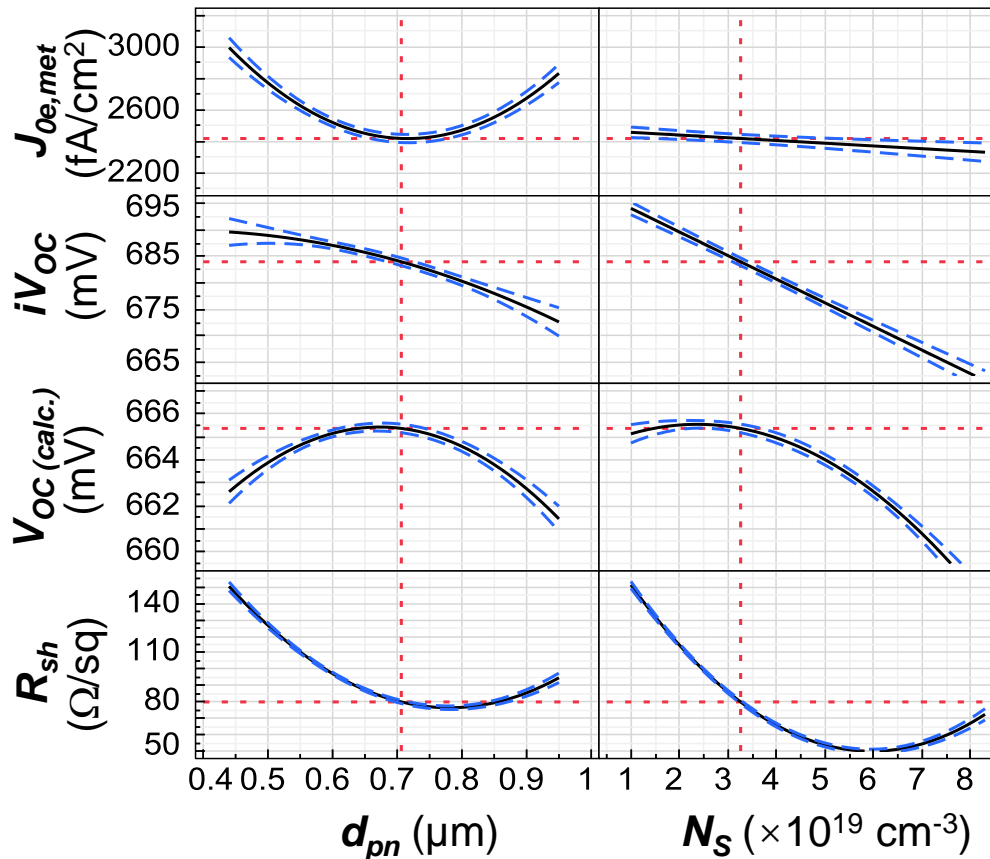
$R_{sh} \uparrow \Rightarrow iV_{OC} \uparrow$ and $J_{0e} \downarrow$

$R_{sh} \uparrow \Rightarrow J_{0e,met} \downarrow \uparrow !!$

no clear trend !

ZEBRA: main limitation

DoE results; response surface model:



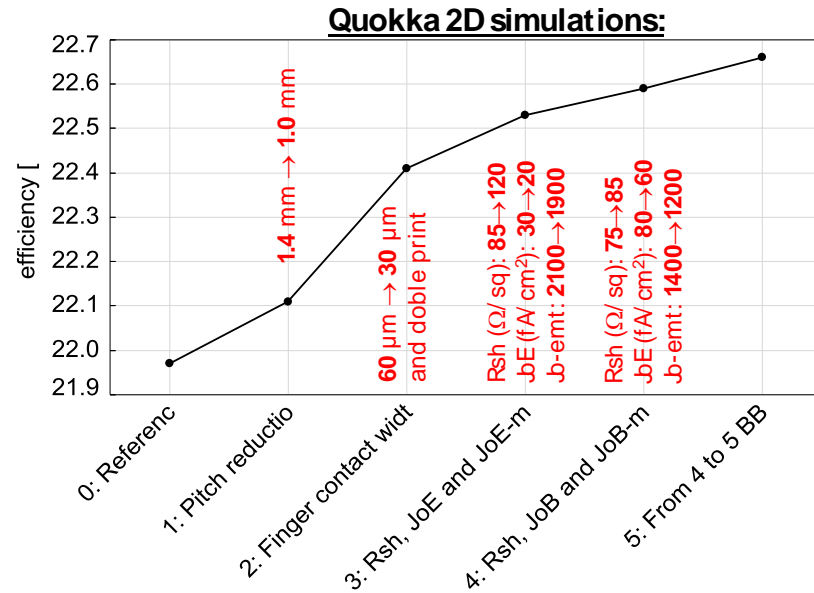
$N_s < 3 \times 10^{19} \text{ cm}^{-3}$ and $d_{pn} > 0.7 \mu\text{m} \Rightarrow J_{oe,met} \uparrow!$

optimum V_{OC} = a trade-off between $J_{oe,met}$ and iV_{OC} !

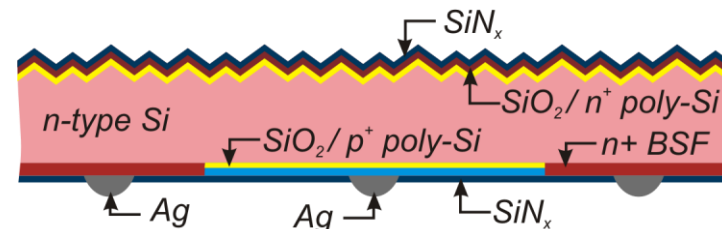
Potential for further improvements

What to do next?

Short term: selective emitter and BSF doping, decrease metal fraction and pitch size \Rightarrow low complexity, low reward.



Longer term: partially or fully* passivating contacts \Rightarrow high complexity, high reward.



*Agnes Merkle *et al.*, this workshop

- ***we developed*** processes to fabricated ***n-type IBC (ZEBRA) solar cells on 156×156 mm²*** (industry-standard) ***using exclusively process steps that are already used in mass production*** for conventional cells.
- ***we achieved 22% conversion efficiency***, with short term potential for >22.5%. The main limitation of the technology today are the recombination losses at the metal contacts (especially at boron emitter contact).
- The ***ZEBRA*** technology ***is ready to be transferred into the industrial production line.***

Acknowledgements



The project HERCULES has received funding from the European Union's Seventh Programme for research, technological development and demonstration under grant agreement No 608498

Thank you for your attention!