

INVESTIGATION OF COEVAPORATED $\text{Cu}(\text{In,Ga})\text{Se}_2$ THIN FILMS IN HIGHLY EFFICIENT SOLAR CELL DEVICES

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In order to further improve $\text{Cu}(\text{In,Ga})\text{Se}_2$ based thin film solar cell devices, it is important to understand the electronic defects in the absorber layer material. For this study $\text{Cu}(\text{In,Ga})\text{Se}_2$ thin film solar cells with a layer sequence $\text{Mo}/\text{Cu}(\text{In,Ga})\text{Se}_2/\text{CdS}/\text{ZnO}/\text{ZnO}:\text{Al}$ have been produced on float glass substrates using a 3-stage coevaporation process together with a precise in-situ laser light scattering process control. The $\text{Cu}/(\text{In}+\text{Ga})$ ratio has been varied by means of altering the duration of the 3rd stage of the process. Although the spectral quantum efficiencies measured on the resulting solar cell devices have not been affected by this variation, the open circuit voltage V_{oc} measured under AM1.5 illumination increases when the final $\text{Cu}/(\text{In}+\text{Ga})$ ratio of the absorber layer is closer to stoichiometry. Indium and gallium depth profiles, which are recorded as energy dispersive X-ray spectroscopy line scans along device cross sections, together with results from previously performed defect spectroscopy on identical samples, are correlated with the increase in V_{oc} .

Introduction

Among thin film technologies, those based on the chalcopyrite Cu(In,Ga)Se_2 (CIGSe) have so far shown the highest efficiencies. For laboratory cells, a record efficiency of 19.5% has been reported [1]. Elaborate means for absorber layer fabrication such as the 3-stage physical vapour deposition (PVD) process have been designed in order to carefully control the properties of the deposited layers [2]. For better understanding of the CIGSe formation during the deposition process, its effects on the absorber layer properties and the final solar cell device performance, CIGSe thin films from various process variations have been carefully studied [2-4]. The presented work provides additional information on this topic.

Similar to experiments performed by Keyes et al. [3], CIGSe absorber layers with a varied Cu-content were prepared. After the Cu-content has exceeded stoichiometry in the 2nd phase of the 3-stage PVD process, and the layer compound is then taken back to a Cu-poor composition by evaporation of In, Ga and Se only during the 3rd phase, complex changes have been observed in the layer morphology, the material's microstructure and the defect chemistry [2-4]. Thus a limited number of samples were processed with different durations for phase 3 and studied by scanning electron microscopy (SEM) and energy dispersive X-ray analysis (EDX) in order to investigate morphology and composition. For characterisation of the defect chemistry and electronic properties, identical samples from the same fabrication processes have recently been investigated using photoluminescence (PL) and capacitance-voltage profiling [5]. The results from both analyses are put into context and discussed in view of the resulting solar cell performance.

Experimental

CIGSe thin films have been grown on Mo coated float glass substrates using a 3-stage PVD process [2,6]. The In-Ga-Se precursors, which result form the 1st phase of the PVD process, are assumed homogeneous in composition if not Ga-rich at the back surface. The Cu-content

of the completed CIGSe layers is determined purely by the duration of the 3rd phase t_3 of the process. The nominal Cu/(In+Ga) (Cu/III) ratios used for the present study are 0.96, 0.92 and 0.88, determined by the ratios of the duration of the phases 3 and 1 (t_3/t_1): 0.20, 0.25 and 0.30. The In, Ga and Se flux in phases 1 and 3 is assumed nominally equal. After having reached the point of stoichiometry in deposition phase 2, Cu and Se evaporation was continued for a duration t_{2a} , with $t_{2a}/t_2 = 0.15$. Laser light scattering is used as in-situ process control [7]. From the CIGSe absorber films solar cells with a total area of 0.5 cm² were prepared by consecutive deposition of a CdS buffer layer by chemical bath, an RF-sputtered transparent ZnO/ZnO:Al bi-layer front contact and, in order to facilitate current collection, an evaporated Ni/Al contact grid. As the CIGSe layers are Cu-deficient in composition no chemical treatment was performed prior to the CdS deposition.

Fig. 1 shows the netto counts of an EDX line scan that has been recorded across a cross section of a complete solar cell device. EDX line scans were acquired in an SEM using an acceleration voltage of 7 keV. This results in a penetration depth of approximately 0.3-0.4 μm and consequently a spatial resolution of approx. 200 nm is assumed. It should be noted that this estimation assumes a homogeneous composition of the analysed volume which is only correct in the bulk area of the absorber. Lines between measurement points are used as guide to the eye.

Solar cell devices were characterised by recording *I-V*-characteristics under standard AM1.5 illumination and external quantum efficiency measurements (EQE).

Results

Morphology and Composition

Fig. 2 shows cross sectional SEM images of the CIGSe solar cell devices with different Cu/III-ratios. Large grains are visible for all three absorbers.

On these cross sections EDX line scans have been acquired, and the netto counts for the In-L and Ga-L peaks, normalized to the Se-K peak, are shown in Fig. 3. Netto counts are directly correlated to the concentration of the corresponding element in the investigated region but do not reflect absolute compositional values. Hence, the line scans in Fig. 3 refer to relative compositional changes in the absorber. Two line scans, recorded on different grains, are shown for each sample in order to illustrate reproducibility across the device. For all three samples studied, Ga has the tendency to accumulate at the back of the CIGSe layer, near the Mo back contact. Simultaneously the In signal decreases for all three samples when moving the probe towards the back contact. What is different for the studied samples is the ratio of the In and Ga signals near the front of the device. For the CIGSe film with a composition nearest to stoichiometry, the Ga/In-ratio near the heterointerface seems to be highest (see Fig. 3a) and decreases when increasing the amount of In-Ga-Se provided in phase 3 (see Fig. 3b and 3c). Finally, Fig. 4 shows the netto count for the Cu-L signal, also normalized to the Se-K peak. These Cu-L netto counts correspond well to the nominal Cu/III-ratios for the different layers. Within the accuracy of the measurements, the Cu distribution across the CIGSe layers does not show any significant gradients.

Defects, Electronic Properties and Solar Cell Performance

Identical devices from the same absorber deposition processes were investigated in order to characterize defects and carrier concentration in relation to the final Cu/III-ratio of the absorber [5]. The authors reported highly non-uniform profiling densities with a minimum at about 400 – 500 nm from the heterointerface. The corresponding capacitance-voltage profiles are shown in Fig. 5. A higher Cu/III-ratio results in a higher profiling density (carrier density + defects). For the device with $\text{Cu/III} = 0.96$, PL measurements at room temperature have identified an additional defect band with an ionization energy of 130 meV, which is tentatively attributed to an acceptor level caused by indium vacancies.

The solar cell performances of the devices corresponding to the different final Cu/III-ratios are given in Table 1. A ‘best’ value is shown for the best cell of each experiment. The ‘average’ shows a mean value for up to 21 cells from the same substrate. For Cu/III = 0.88, some cells were shunted due to macroscopic device defects and were hence not included in the average. Variations in the total area j_{sc} are attributed to a slight error in the patterning process. However, general trends in V_{oc} and FF seen in the ‘best’ devices are confirmed by the ‘average’ results. The V_{oc} can be seen to increase along with increasing Cu/III. Simultaneously, the fill factor FF decreases. In Fig. 6 on the other hand, the EQEs for the devices from the absorber layers with varied Cu/III-ratio, indicate the same photoactive energy band gap at 1.19 eV.

Discussion

In agreement to what was reported above, for In-Ga-Se precursors that are homogeneous in composition and for those which are Ga rich near the back surface, Ga accumulation near the back contact within the final CIGSe film has also been observed by other authors [2]. The fact that the gallium signal is reduced in relation to the indium signal near the heterointerface as the duration of the 3rd deposition phase increases (compare Fig. 3a to 3c), implies an effective transport mechanism of Ga towards the back of the sample. This proportional decrease in the Ga signal, and hence a reduced energy band gap of the material at the heterointerface, qualitatively agrees with the change seen in the open circuit voltage V_{oc} of the devices. From the EQE (Fig. 6) it is however clear that the minimum band gap in the photoactive region of the resulting CIGSe devices is not affected. Considering that the In/Ga-ratio of the evaporation flux is kept constant during phase 3 and that the variation in the deposition processes is only in the duration of this final process phase, the latter seems to be the main determining factor for the development of the In-Ga-profiles. This means, that, once the sample is nominally Cu-poor in phase 3, Ga diffusion towards the back contact does not

necessarily cease and that a further study of the compositional gradients within the surface near region of the CIGSe layers with a method that provides higher spatial resolution is of immense interest.

The increase in the profiling density that is seen for the Cu-rich samples (Fig. 5) also correlates with the increase in V_{oc} (Table 1). As for the sample with $\text{Cu/III} = 0.96$, this may be related to the presence of indium vacancies and since in Fig. 3 the In/Ga-ratio is lowest in the absorber region near the heterointerface, a spatial localisation for the indium vacancies in this area is suggested.

We attribute the decrease in the FF for high Cu/III-ratios to additional shunt paths that may be caused by the presence of remaining areas of Cu_xSe , e.g. along grain boundaries [5].

Conclusion

The investigation of CIGSe based thin film solar cell devices, prepared from absorber layers with different Cu/III-ratios after phase 3 of the 3-stage coevaporation process, shows a correlation of the duration of phase 3, i.e. the final Cu-content of the absorber, with the In and Ga profiles as they are measured by EDX line scans on device cross-sections. An increased V_{oc} is obtained for the devices with the absorber composition closest to stoichiometry. This coincides with a lower In/Ga-ratio near the heterojunction when compared to samples from processes with a more extended phase 3, plus an increased profiling density within the absorber. The latter is associated with an additional defect level, possibly indium vacancies.

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Tables

Table 1: Characteristic values for the I-V measurement of cells made from absorbers with different Cu/(In+Ga) ratios; $j_{sc}(\text{active})$ refers to the current density that is determined via the external quantum efficiency, whereas $j_{sc}(\text{total})$ is determined by the measured I-V characteristic considering the total device area.

Cu/III	Best				Average				
	V_{oc} [mV]	FF [%]	$j_{sc}(\text{active})$ [mA/cm ²]	η [%]	Number of cells	V_{oc} [mV]	FF [%]	$j_{sc}(\text{total})$ [mA/cm ²]	η [%]
0.96	701	74.3	32.1	16.8	21	700	73.8	31.6	16.3
0.92	684	75.8	32.0	16.7	21	683	75.7	32.4	16.7
0.88	667	76.5	32.1	16.6	16	665	76.1	31.8	16.1

Figure Captions

Figure 1: Netto counts of an energy dispersive X-ray analysis linescan across a CIGSe thin film solar cell device.

Figure 2: Cross sectional scanning electron microscopy images of solar cells from Cu(In,Ga)Se₂ layers with different Cu/(In+Ga) ratios(Cu/III).

Figure 3: Energy dispersive X-ray analysis linescans across the Cu(In,Ga)Se₂ devices with varied phase 3 duration; displayed are the netto counts recorded for the In-L and Ga-L peaks after normalization to the Se-K peak.

Figure 4: Energy dispersive X-ray analysis linescans across the Cu(In,Ga)Se₂ devices with varied phase 3 duration; displayed are the netto counts recorded for Cu after normalization to the Se peak.

Figure 5: Capacitance-voltage profiles acquired at room temperature using a measurement frequency of 100 kHz [5].

Figure 6: External quantum efficiency of the solar cell devices from Cu(In,Ga)Se₂ layers with different Cu/(In+Ga) ratios (Cu/III).

Figure 1

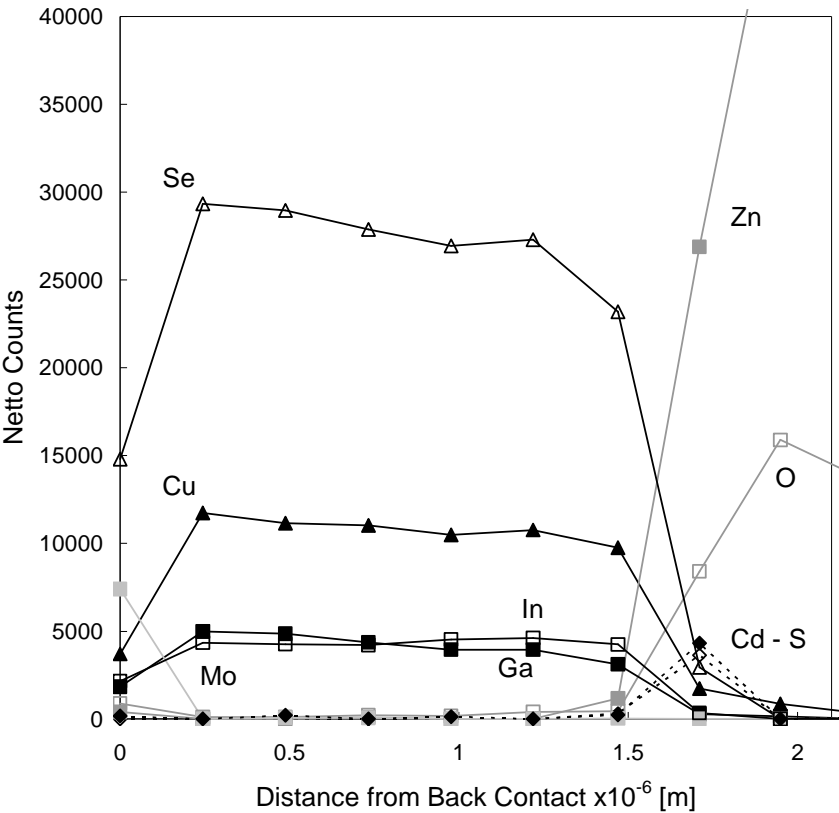


Figure 2

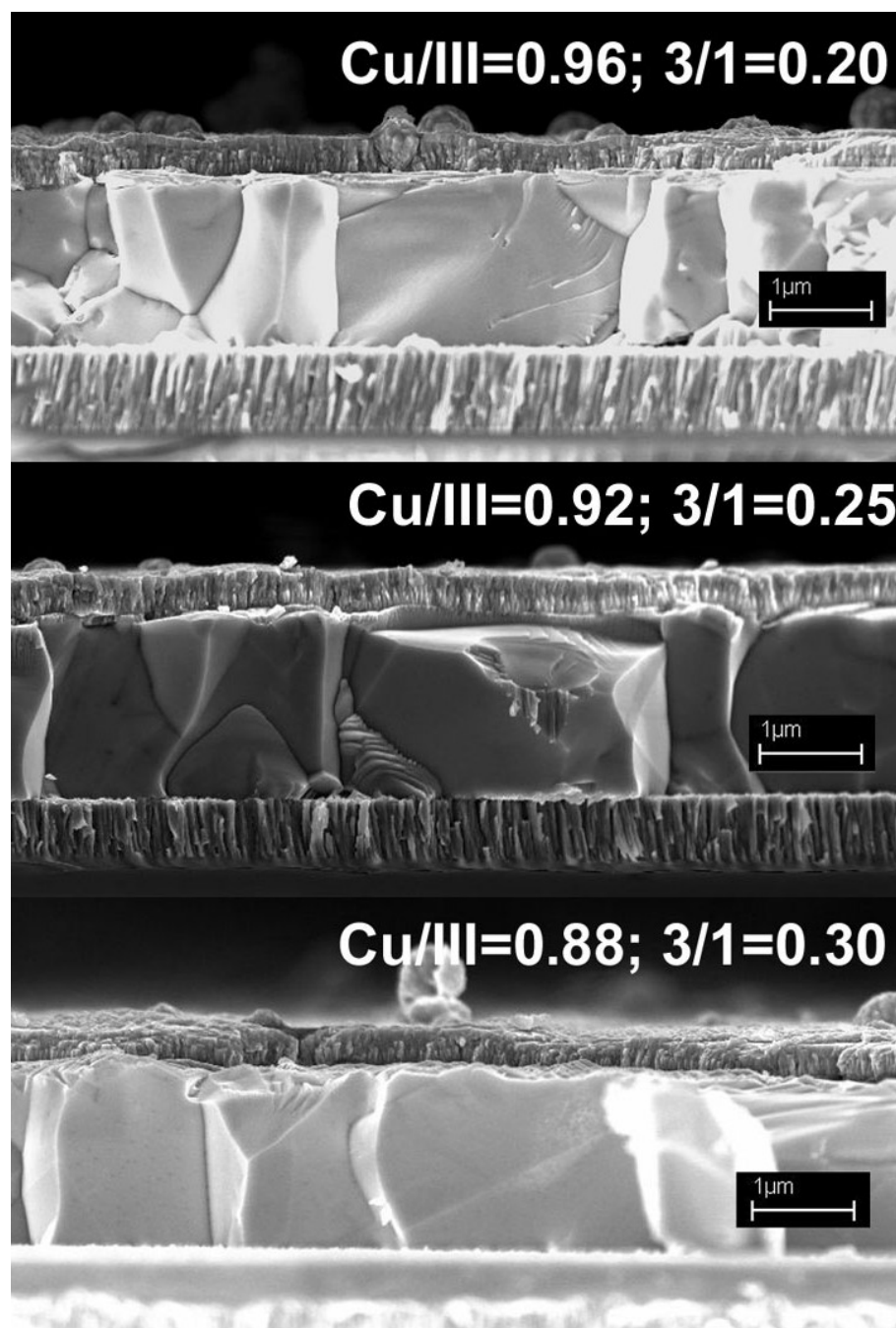


Figure 3

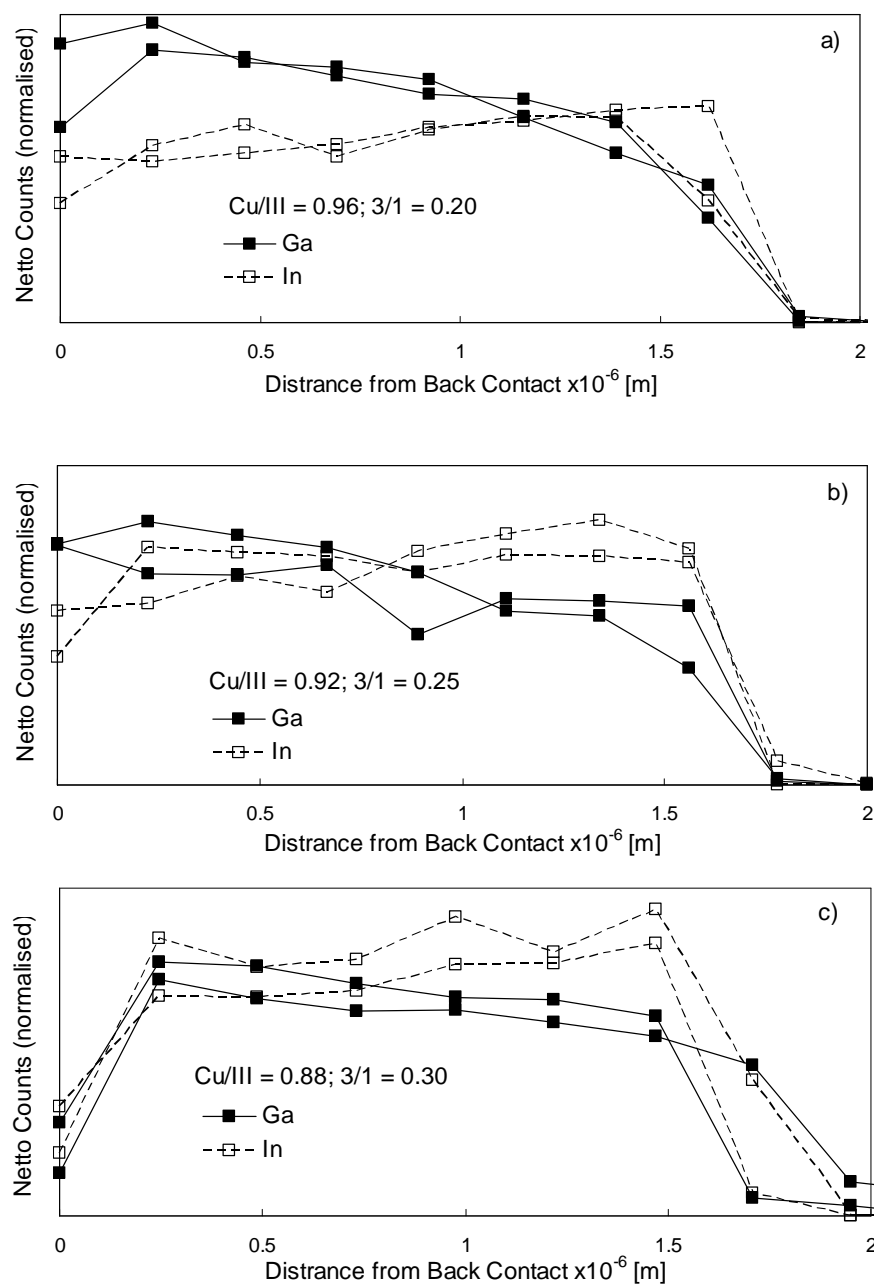


Figure 4

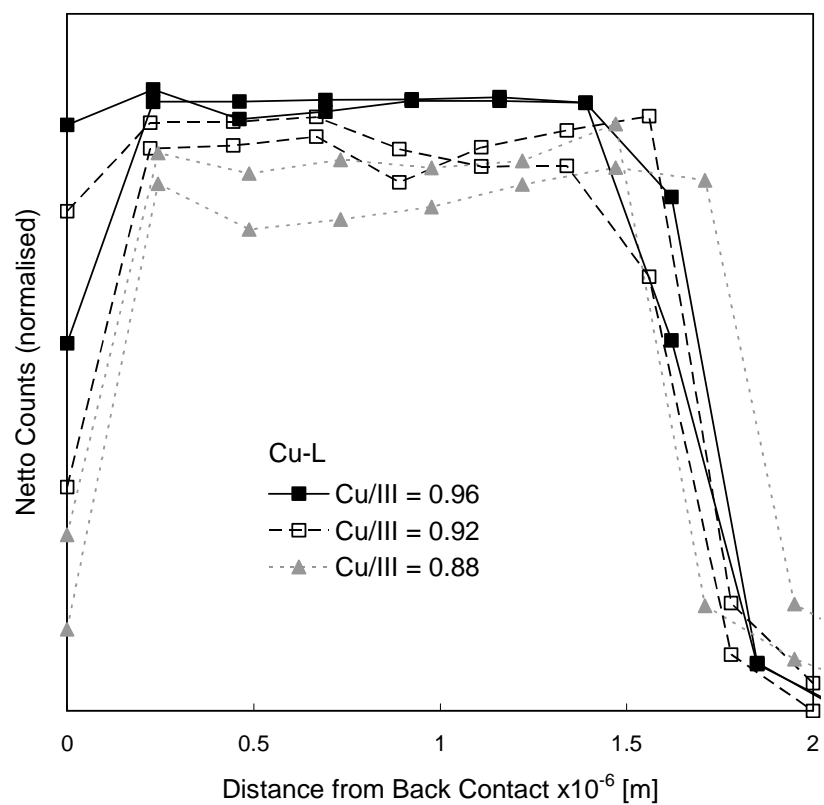


Figure 5

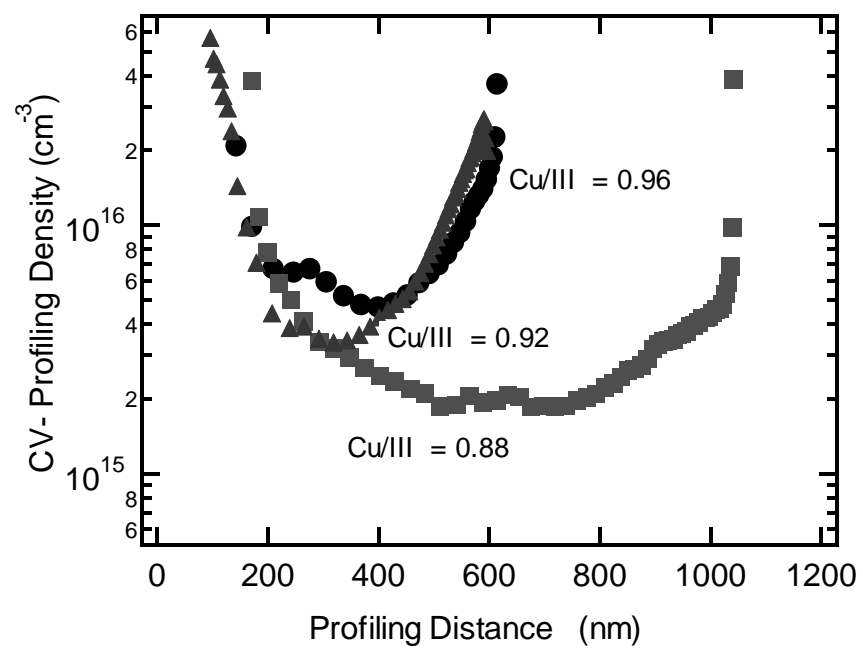


Figure 6

