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**A progress report on implementing inkjet-printed transparent conductive electrodes in solution-processed organic electronics**

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The field of printed organic electronics has, through the use of solution-based materials, not only made new devices accessible, but also allowed the process of manufacture to move towards a high throughput industrial scale. However, while solution-based active layer materials in these systems have been studied quite intensely, the printed electrodes and specifically the transparent conductive anode have only relatively recently been investigated. In this progress report, we will highlight the use of metal nanoparticles within printed organic electronic devices, specifically their use as replacement of the commonly used indium tin oxide transparent conductive electrode within organic photovoltaics (OPVs) and organic light emitting diodes (OLEDs). A cross fertilization between the applications is expected since an OPV device is essentially an inversely operated OLED. The report aims to highlight the use of inkjet-printed nanoparticles as cost-effective electrode for printed opto-electronic

applications and discuss methods to improve the conductive and interfacial properties.

Finally, in an outlook, we will investigate the use of these types of metal nanoparticle inks to manipulate light management properties, such as outcoupling, in the device.

## **1 Introduction – solution processed organic electronics**

The field of organic electronics has gained substantial momentum in the past 20 to 30 years. The discovery of electroluminescent polymers in the early 90s<sup>[1]</sup> has led to commercialization of the organic light emitting diode (OLED),<sup>[2]</sup> leading to modern day OLED applications which include the displays used in smartphones, tablets and e-readers, in foldable touch screens and in some curved lighting devices.<sup>[3-7]</sup>

The other main branch of organic electronics, organic photovoltaics, has not been quite as commercially successful, even though new efficiency records are being set all the time.<sup>[8]</sup> However, brought on through the latest advent of perovskites,<sup>[9-11]</sup> this new branch of hybrid organic photovoltaic device promises to give a newfound resurgence to the field of organic / hybrid photovoltaics.<sup>[12]</sup>

One of the major advantages of these types of electronic devices is that they can be manufactured using a solution-based processing route, i.e. from solutions or inks or even pastes that contain the active material dissolved in a suitable solvent system. These solutions can then be deposited using standard lab-based techniques such as spin coating, but also using sophisticated, technology-relevant processing systems such as inkjet-printing or roll-coating.<sup>[13-14]</sup> Organic field effect transistors (OFETs), highly efficient organic photovoltaics (OPVs) and high performance OLEDs have been fabricated using inkjet printing, thereby underlining the versatility of this printing method.<sup>[15-20]</sup>

### *1.1 A brief overview of device functionality*

However, especially in high efficiency OLED structures, vacuum-processed stacks of materials are usually utilized, with at least one organic semi-conductor layer sandwiched

between a transparent conductive oxide (TCO) – which serves as the transparent conductive anode – and usually a vacuum-deposited low work function metal – which serves as the cathode. The organic active layers are either deposited by vacuum sublimation of small molecules or in a wet chemical process from a solution of conjugated polymers.

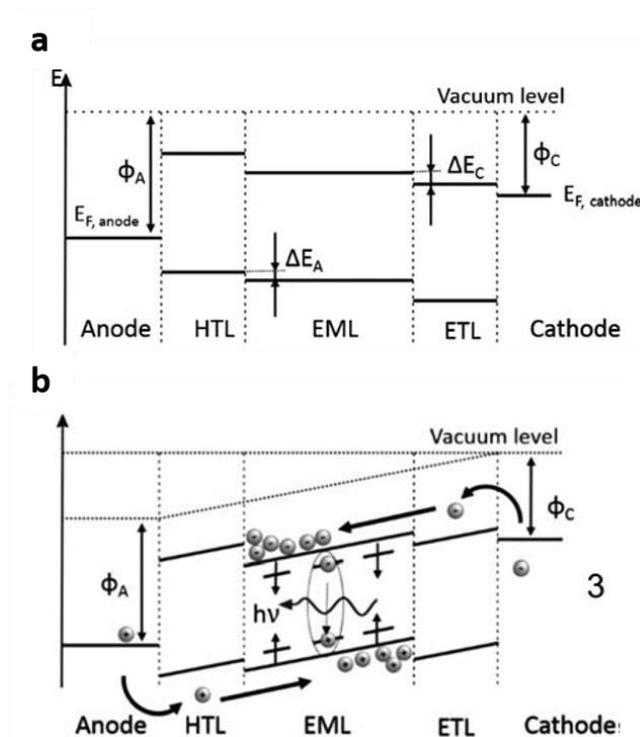


Figure 1: In flat-band conditions (a) electrons and holes encounter an energy barrier ( $\Delta E_A$  and  $\Delta E_C$ ), while under an applied external bias (b), this energy barrier can be overcome and holes and electrons can recombine in the emitter material (EML).<sup>[21]</sup> – Reproduced and modified by permission of The Royal Society of Chemistry.

In a traditional multilayer OLED the emissive layer (EML) is sandwiched between a hole transporting layer (HTL) and an electron transporting layer (ETL). At flat-band conditions, i.e. no external bias, energy barriers between the HTL and EML as well as the ETL and EML can be identified (see Figure 1). When an external bias is applied, the energy barriers can be overcome and the injection of charge carriers can take place, leading to photons ultimately generated by the recombination of the injected charge carriers within the active layer.<sup>[22-24]</sup> Using this architecture, the injection of holes and electrons is facilitated, leakage of the “wrong” carrier towards the electrode is prevented and any recombination/quenching processes occurring at the electrodes are avoided.<sup>[25-27]</sup>

An OPV device is essentially an inversely operated light emitting diode (see Figure 2). In OPVs, the incident photons ultimately lead to free charge carriers which contribute to an external current. As opposed to silicon-based solar cells,<sup>[28]</sup> in OPV bulk heterojunction systems consisting of a conjugated polymer donor/fullerene acceptor organic semiconductor blend, the photo-excitation of an electron occurs from the donor HOMO into its LUMO after light absorption to form an exciton.<sup>[29-32]</sup> The excitons diffuse within the donor phase and dissociate upon encountering an interface with the fullerene acceptor.<sup>[33]</sup> Charge transfer then results in an intermolecular charge-transfer (CT) state, whereby the electron and hole are located on the acceptor and donor, respectively.<sup>[34-35]</sup> However, as the charges may still be bound to one another via Coulomb forces, an electric field is needed to separate them into free charges.<sup>[36]</sup> Finally, the free charges (electrons and holes) have to be transported towards the carrier selective electrodes along their respective domain. Those excitons that do not reach the bulk heterojunction interface recombine and therefore do not contribute to the photocurrent.<sup>[37]</sup>

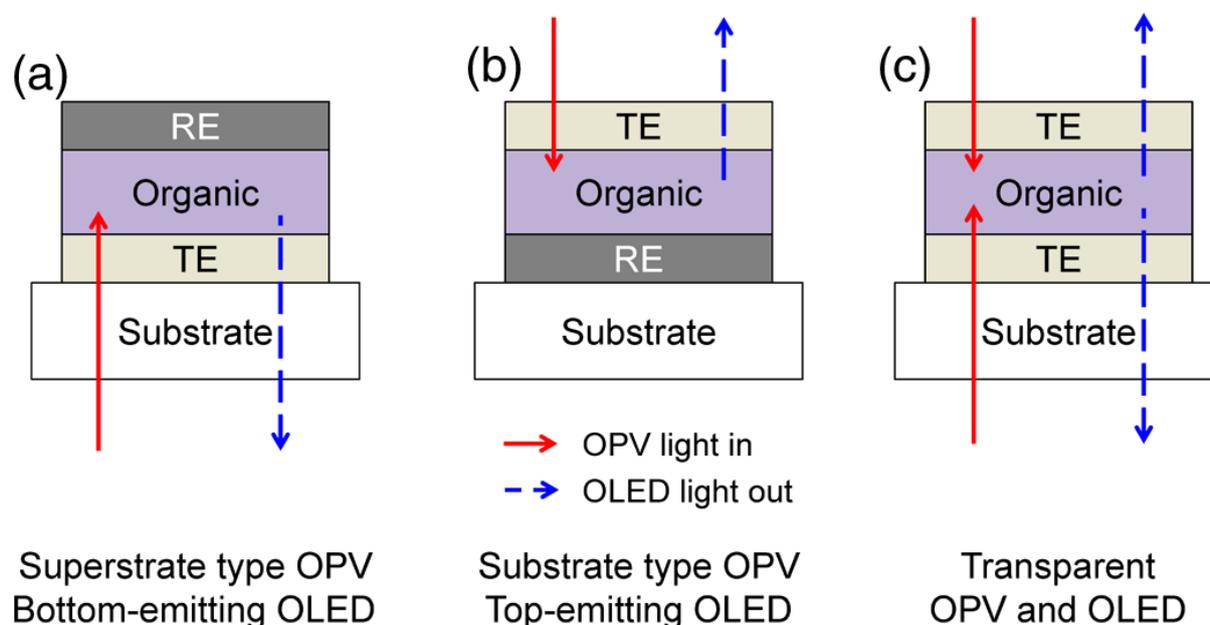


Figure 2: The common device architectures employed for OPVs and OLEDs underline the reciprocal nature of their functionality. (a) light transfers into the superstrate-type OPV and out of the bottom-emitting OLED through the transparent substrate and transparent electrode (TE), while being reflected off the reflective electrode (RE), (b) light transfers into the substrate type OPV and out of the top-emitting OLED through the

top transparent electrode; and (c) light transfers in and out of the transparent OPV and OLED through both the bottom and top transparent electrodes.<sup>[38]</sup> – Reproduced by permission of SPIE Digital Library.

## 1.2 Some examples of solution-processed devices

Since the functional layers in solution-processed OLEDs and OPVs are deposited one on top of another, it is imperative that one deposited layer does not chemically or physically interact with the previously deposited layer underneath it. In the case of solution-based processing this is not trivial. For example, water-based hole-transporting layers such as poly(3,4-ethylenedioxythiophene):poly(styrenesulfonate) (PEDOT:PSS) have a strong influence on some active material classes, such as perovskites.<sup>[39-40]</sup> In order to tackle the issue of dissolution of already applied layers during the fabrication of well-defined multilayer structures in a solution-based process, Auer-Berger *et al.* looked at several mechanisms and methods to enable the fabrication of efficient multilayer (double and triple active layer) OLED structures.<sup>[21]</sup>

One such example utilizes crosslinking to thermally stabilize a hole-transporting compound via annealing at 200 °C, after which it is completely insoluble to organic solvents. By spin-coating a blue emissive compound on top, highly efficient fluorescent blue emitting 2-layer OLEDs were produced.<sup>[41]</sup> The top electrode consisted of thermally evaporated Cs<sub>2</sub>CO<sub>3</sub>/Al, while the transparent anode was ITO covered with PEDOT:PSS.

The same thermal stabilization approach can be utilized to sandwich an emissive layer in between a crosslinked and insolubilized hole-transporting layer, while depositing a polar-soluble, i.e. orthogonal, electron-transporting layer on top.<sup>[42-43]</sup> OLEDs based on such an architecture showed an increase in the rate of exciton generation by facilitating charge carrier confinement in the emissive layer, thereby improving recombination and proving the effectiveness of this multilayer approach.

These examples highlight the importance of understanding the interface behavior between the individual components of a multilayer optoelectronic device. However, all devices discussed

are based on ITO/PEDOT as the transparent anode material system. We will now discuss in detail the effect of replacing the ITO in both OPV and OLED systems with inkjet-printed structures and its effect on the interfacial properties and resulting device performance.

## 2 The printed transparent conductive electrode in organic electronics

### 2.1 Inkjet-printing

Inkjet-printing is an additive, direct write, non-contact technique, compatible with printing manufacturing and requiring no masks or lithographic pre-patterning of substrates. It incorporates drop-on-demand (DoD) technology, which enables ink droplets to be deposited exactly where required on the desired substrate, minimizing material loss as a consequence.<sup>[44]</sup> Inkjet-printing can also be transferred to a roll-to-roll (R2R) process,<sup>[45-46]</sup> thereby accessing both a productivity of up to 6 m<sup>2</sup>/min and also small feature sizes (30-50  $\mu\text{m}$  line/10  $\mu\text{m}$  space) and a wide variety of shapes.<sup>[47-50]</sup> Inkjet printing has been utilized in order to fabricate every component of an organic optoelectronic device, including the active layer materials and the metal top and transparent bottom electrodes.<sup>[16-17, 51-52]</sup> However, only very few devices have claimed to be wholly inkjet-printed,<sup>[53-55]</sup> paving the way for fully solution-processed devices to become more widespread.

One of the key differences to other solution-based deposition techniques such as spin coating and doctor blading is that solution deposition occurs automatically through a printhead with a particular drop volume and nozzle number (see Figure 3). Solution viscosity of the inks is a crucial parameter, as the piezoelectric mechanism must be able to eject the solution sufficiently out of the nozzles. Since the temperature of the cartridge can also be adjusted, this viscosity can be altered (in most cases decreased by increasing the temperature) to provide more desired properties.

The second key difference to other deposition techniques is that deposition occurs in a line-by-line fashion (much like an inkjet printer in the home) and therefore each printed line must

merge with the previous and next printed line in order to form a homogenous and morphologically smooth film on the substrate.<sup>[52]</sup>

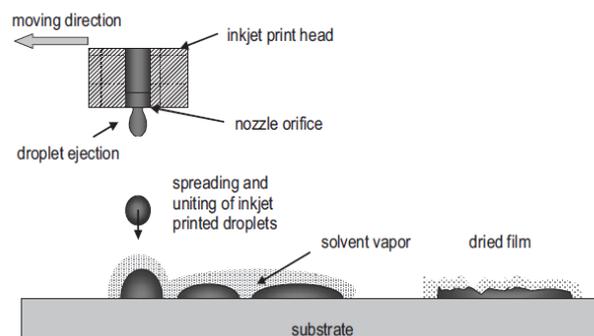


Figure 3: During the inkjet printing process the printhead moves across the substrate, ejecting single droplets, which then merge and form a dried film after solvent evaporation.<sup>[56]</sup> Reproduced by permission of Wiley VCH.

Overall, it is possible to control the temperature of the printhead/cartridge and the temperature of the substrate. The range of temperature control of the substrate usually does not extend beyond 60-80 °C, since above this temperature undesired evaporation of solvent can occur at the printhead nozzles, leading to clogging. Furthermore, the drop spacing, the distance of the printhead to the substrate and the amounts of nozzles used for deposition (each nozzle can be individually addressed). It is also possible to adjust each nozzle's individual firing voltage to eject the droplets as well as the overall waveform, which provides details on the amount of pressure used to eject each droplet.

## 2.2 Transparent conductive electrodes and printed metal grid design

As shown above, most high efficiency organic electronic device architectures are based upon ITO as the transparent electrode. ITO, however, has an inherent limit in its range of applications due to its brittle nature, cost and its low-throughput method of production.<sup>[57-58]</sup> With a sheet resistance of  $\sim 5 \Omega/\square$  it is suitable for small area devices, but at larger scale, where higher conductivity is essential, this becomes a limiting factor.<sup>[59]</sup> It is therefore desirable to replace ITO with other materials, which is especially important when targeting

flexible OLED lighting or smart packaging applications. Several other types of transparent electrodes have been investigated, including high conductivity polymers such as PEDOT:PSS,<sup>[60-62]</sup> graphene-based and other novel material electrodes,<sup>[63-64]</sup> carbon nanotubes,<sup>[65]</sup> Ag and Cu nanowires,<sup>[66-67]</sup> Cu mesh,<sup>[68]</sup> and a variety of thermally evaporated metal grids.<sup>[69-71]</sup> Transparent metal oxides, used together with a metal (grid) contact, can also be evaporated or sputtered, while some metal oxides such as ZnO, Al-doped ZnO and WO<sub>3</sub> are routinely being deposited by solution-processing, including inkjet-printing.<sup>[53, 72-74]</sup> Solution-processed metal nanoparticles in the form of printable inks have also been investigated,<sup>[55, 75]</sup> but most approaches utilize photolithography and require several (subtractive rather than additive) processing steps.<sup>[76-79]</sup> The implementation of a metal nanoparticle-based anode is usually made by creating a printed structure in the form of a grid or mesh and then over-coating this with a transparent conducting material, such as PEDOT:PSS. This serves two purposes: firstly, it “spreads” the area of conductivity from the direct metallic areas of the grid itself to cover also the areas where no metal structure is present, secondly, it helps to planarise the printed structure, by over-coating small defect sites and thereby enabling a smooth contact area with the following organic layer.

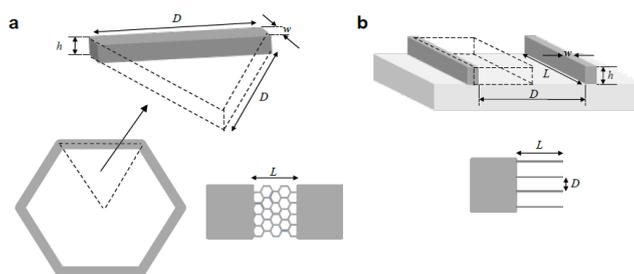


Figure 4: When implementing a hexagonal grid (a) or a straight line architecture (b) as the printed conducting electrode, the spacing  $L$  between busbars, spacing  $D$  between lines and grid as well as the width  $w$  and height  $h$  of the structures themselves need to be taken into consideration.<sup>[80]</sup> – Reproduced by permission of I.

Burgués Ceballos.

The two geometries shown in Figure 4 have gained particular attention: a straight line layout and a honeycomb layout, which have been explored in great detail by Burgués Ceballos *et*

*al.*<sup>[80]</sup> The honeycomb design provides an advantage versus the straight-line geometry: any possible breaks in the line that have occurred during the printing or drying process do not influence the overall current collection efficiency as significantly, since charges have the possibility to be rerouted through alternative paths along the honeycomb grid. However, a straight-line geometry is simpler, easier to print and more easily adjusted regarding size of the grid itself and the techniques used to prepare the grids.

Burgués Ceballos *et al.* estimated the losses sustained by a Ag grid/PEDOT:PSS electrode due to shadowing and resistive effects by adapting a numerical model,<sup>[81]</sup> and using experimentally obtained input parameters.<sup>[80]</sup> Some typical OPV performance parameters were also considered in the model based on reference devices.<sup>[82]</sup>

In the case of the linear geometry, the minimum losses of  $\pm 3\%$  were situated in a wider pitch range of between 720  $\mu\text{m}$  and 2480  $\mu\text{m}$  (see Figure 5), while the corresponding range of minimum losses was much lower in the honeycomb structure (180 to 1110  $\mu\text{m}$ ). However, the losses due to shadowing and sheet resistance contributed more markedly at the extremes in the honeycomb structure.

Moreover, as the insets of Figure 5 show, by simply changing the line width the linear geometry allows a wider range of optimum pitch to be accessed. This is important for two reasons: firstly, by widening the lines, a lower line density can be employed in a linear geometry, leading to a reduced use of metal ink; secondly this lower line density can improve the success of the imprinting transfer described later in the text.<sup>[80]</sup> On the other hand, employing a honeycomb structure can reduce the total losses below 2% when very thin lines are deposited, which is in stark contrast to the minimum 7.6% achievable by using the linear geometry.

Overall, two distinct regimes were differentiated: The honeycomb geometry yields lower total losses when employing line widths below 50  $\mu\text{m}$ , while above that width the linear geometry

shows lower total losses. For very thin lines and the low losses attributed to those, the use of the honeycomb structure may be preferential.<sup>[80]</sup>

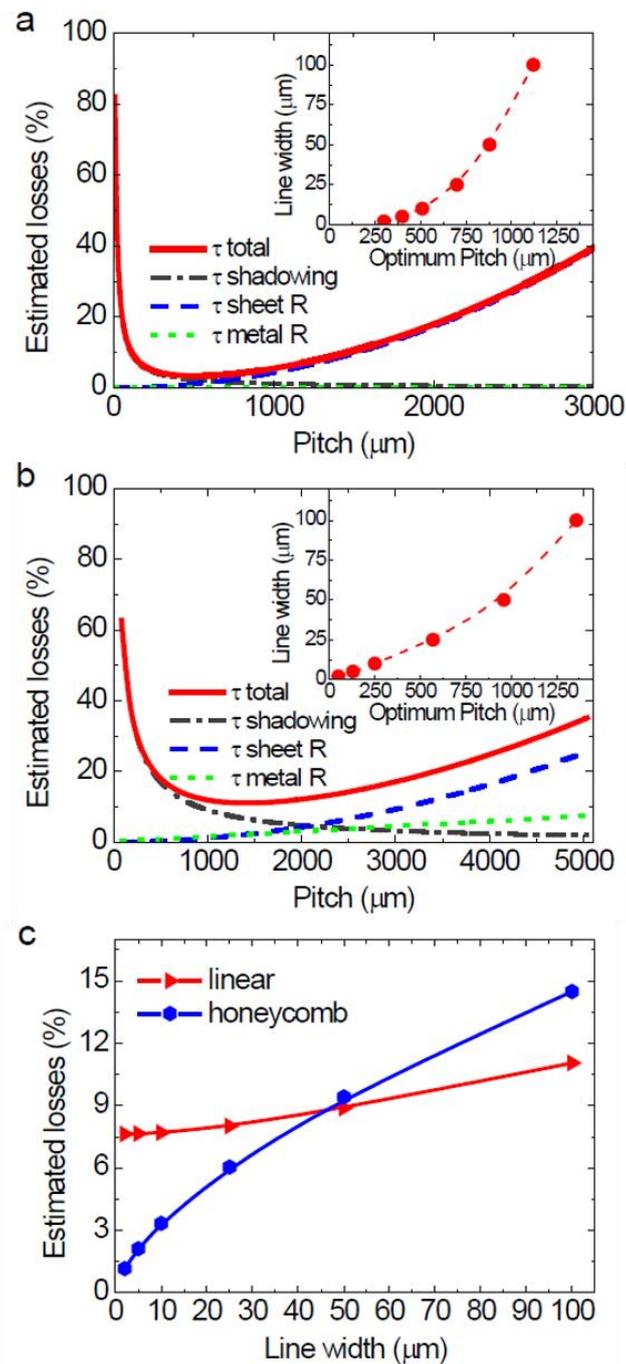


Figure 5: The calculated contribution of the shadowing and resistive losses as a function of pitch show a greater difference at the pitch extremes for the honeycomb (a) grid architecture than for the lines (b). At the same time overall losses are smaller over a wider range of line widths in the case of the linear architecture than for honeycomb, allowing a greater processing window (c).<sup>[80]</sup> – Reproduced and modified by permission of I. Burgués Ceballos.

### 2.3 *Metal nanoparticle inks*

The discussed grids need to be deposited from an ink and the majority of ink formulations can be divided into roughly two categories: inks containing capped metal nanoparticles and metal organic decomposition inks.<sup>[83]</sup> Generally, the inks contain a low metal content and (organic) stabilizers to prevent nozzle clogging during printing and to comply with printhead restrictions concerning viscosity and surface tension. Some inks are available with up to 70 wt.% metal loading, but these require control of any sedimentation process that may occur. During a sintering process which commonly follows the ink deposition, the deposited ink is transformed into a uniform and conductive film suitable for use in electronic structures. In the case of capped metal nanoparticles the capping is removed, while for the metal organic decomposition the ink precursors decompose to form the final film.

A wide variety of sintering treatments (laser, flashlamp, microwave, thermal, plasma, electrical, chemical) have been reported for metal particle inks,<sup>[83-89]</sup> and all of these methods have varying advantages and disadvantages concerning throughput and geometry dependency. The specific nature of each sintering treatment (e.g. photonic vs. chemical) has an effect on the duration of treatment and its suitability. For example, laser sintering is slow (ca. 10-15 minutes per A4 sheet) compared to flash light sintering (in seconds) but has the benefit that different structures can be treated in an optimized manner.<sup>[84]</sup> This is due to photonic sintering being geometry dependent, meaning that different line widths and film thicknesses possess different optimal parameters regarding time and intensity of the treatment.

Photonic sintering additionally requires substrates with a defined and suitable thermal conductivity, since the control of the dissipation of a high amount of photo-generated heat in a short period of time during the sintering process is critical.<sup>[90]</sup> If the thermal conductivity is too high, it can prevent enough heat from sintering the ink due to rapid dissipation. However, flexible substrates with low thermal conductivity can be subject to rupture or delamination of the formed structures. For silver sintering, due to the metal's high oxidation resistance and

good electrical conduction even in oxidized states,<sup>[91]</sup> simple thermal processes can be applied making the required sintering step to form uniform films of merged nanoparticles less demanding.

### **3 Previous work on printed transparent conducting electrode grids**

Past approaches to printable conductive metal structures to provide a grid-based transparent anode have focused mainly on silver due to the abovementioned advantages. An early proceedings paper reported organic light emitting diodes containing silver nanoparticle grids as an ITO replacement.<sup>[92]</sup> However, while those early results showed how applicable the proposed maskless deposition method was for large area devices, no specific information was given regarding the efficiency of the OLEDs based on the Ag grid line/PEDOT:PSS electrodes versus ITO. By combining the metal grids with a highly conducting PEDOT:PSS as the transparent anode electrode, hole-injection into the emitting material was facilitated. OLEDs with an active area of several cm<sup>2</sup> were produced and scaled to approximately 150 cm<sup>2</sup>, including on flexible PEN substrate. The grid lines were 120 nm in height and were able to be sufficiently overcoated with a thick layer of PEDOT:PSS.

#### *3.1 Overcoating – control of the printed metal grid line height*

Indeed this direct overcoating has been reported in a variety of publications that have dealt with inkjet-printed and sintered silver grids based on nanoparticle inks for use in organic electronic devices (see Table 1). The height and width of most grid lines was approximately in the range of 100-400 nm and 50-200 μm, respectively. When utilized in solar cell applications, this height regime can yield an equivalent device performance to ITO-based devices. This is achieved by covering the grids with a layer of PEDOT:PSS which fully coats the grid lines in order to prevent shunts (see Figure 6).

Table 1: The tabular overview compares inkjet-printed transparent conducting electrodes based on Ag ink, which have been subjected to a simple overcoat of the subsequent layer. Unless indicated, the printed electrode functions as the bottom transparent anode in the device. For comparison regarding line dimensions, photolithography is included in one instance.

Reference	Deposition method	Device	Material	Grid type	width/space	Height	Sheet resistance	Transmission
Harkema SPIE 2009 <sup>[92]</sup>	Inkjet	OLED	Ag ink (Cabot AG-IJ-G-100-S1)	lines	200 $\mu\text{m}$ / -	120 nm	1 $\Omega/\text{sq}$	-
Galagan SEMSC 2012 <sup>[93]</sup>	Inkjet	OPV	Ag ink (Suntronic U5603)	hexagonal	215 $\mu\text{m}$ / 5 mm	250 – 400 nm	5-12 $\Omega/\text{sq}$	-
Galagan AEM 2012 <sup>[47]</sup>	Inkjet	OPV	Ag ink (Suntronic U5603)	lines	300 $\mu\text{m}$ / $\geq 1$ mm	500 nm	1-20 $\Omega/\text{sq}$	-
Neophytou APL 2012 <sup>[16]</sup>	Inkjet	OPV	Ag ink (Suntronic U5603)	lines	50 $\mu\text{m}$	200 nm	-	> 90%
Galagan SEMSC 2014 (top electrode) <sup>[94]</sup>	Inkjet	OPV	Ag ink (Suntronic U5603)	lines	100 $\mu\text{m}$ / 2mm	200 nm	-	> 80%
Neophytou SEMSC 2014 <sup>[17]</sup>	Inkjet	OPV	Ag ink (Suntronic U5603)	lines	50 $\mu\text{m}$	200 nm	8.5 $\Omega/\text{sq}$	> 90%
Sam OE 2014 <sup>[76]</sup>	Photolithography	OLED	Evaporated Ag	hexagonal	2 $\mu\text{m}$ / 60 $\mu\text{m}$ 3 $\mu\text{m}$ / 10 $\mu\text{m}$	10 nm 40 nm	200 $\Omega/\text{sq}$ 7 $\Omega/\text{sq}$	> 94%
Eggenhuisen JAMC 2015 <sup>[54]</sup>	Inkjet	OPV	Ag ink (Suntronic U5603)	lines	-	-	7 $\Omega/\text{sq}$	-
Patil OE 2015 (top and bottom electrodes) <sup>[95]</sup>	Inkjet	OPV	Ag ink (Suntronic U5603)	lines	250 $\mu\text{m}$ / 2 mm	250-300 nm	0.63 $\Omega/\text{sq}$	> 85%
Hermerschmidt FPE 2016 <sup>[15]</sup>	Inkjet	OLED	Ag ink (Suntronic U5603)	lines	90 $\mu\text{m}$	70-80 nm	0.2 $\Omega/\text{sq}$	> 80%
Polino AMI 2016 (top electrode) <sup>[96]</sup>	Inkjet	OPV	Ag ink (Suntronic U5603)	lines	100 $\mu\text{m}$ / 2mm	400 nm	0.27 $\Omega/\text{sq}$	-
Kinner APL 2017 <sup>[49]</sup>	Inkjet	OLED	Ag ink (Cabot CCI 300)	hexagonal	350 $\mu\text{m}$ / 3-8 mm	400-800 nm	< 5 $\Omega/\text{sq}$	> 75%

However, this same height of the combined grid line/PEDOT:PSS electrode can usually not simply be transferred to OLED applications. This is because arguably in OLEDs the accuracy of the PEDOT:PSS layer thickness plays a much greater role than in solar cells. Not only does the layer need to be transparent and conductive enough, but the properties of hole-injection

need to be optimal across a greater voltage range as well as being balanced to the device's electron-injecting characteristics.

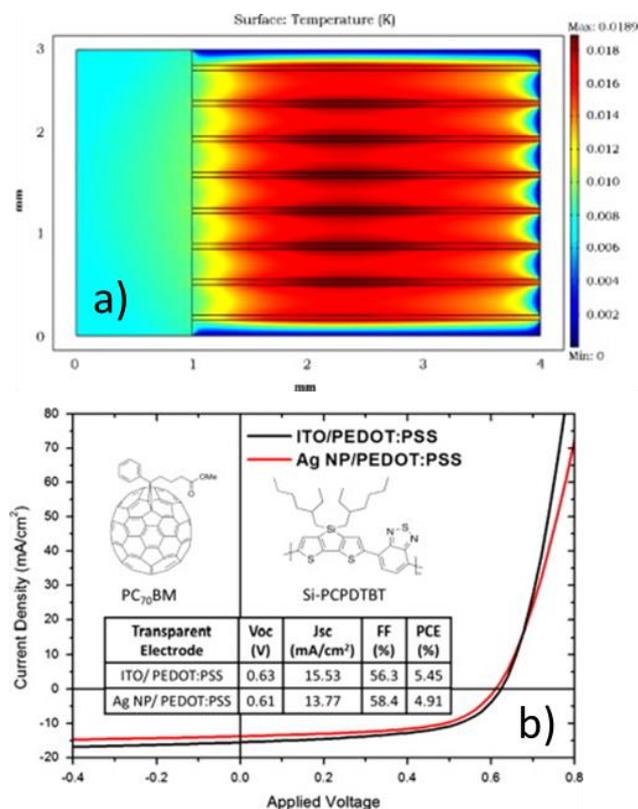


Figure 6: a) A homogenous temperature distribution is achieved within the transparent electrode used in OPV devices based on an 8-line Ag grid coated with PEDOT:PSS, b) both ITO-free and ITO-based OPVs containing 8-line Ag grid/PEDOT:PSS electrodes show similar device performance parameters.<sup>[17]</sup> – Reproduced and modified by permission of Elsevier.

The electrode therefore usually needs to be entirely re-examined in order to establish the required characteristics to achieve a similar device performance to ITO-based OLEDs. One aim is to reduce the height of the grid electrode in order to cover it with a minimum amount of PEDOT:PSS, so as to remain comparable to the common reference PEDOT:PSS thickness of approximately 50 nm on ITO. One approach has therefore been to print grid lines on top of glass substrates previously treated with UV-O<sub>3</sub>, which results in an increase of surface free energy and better wetting of the nanoparticle ink (see Figure 7).

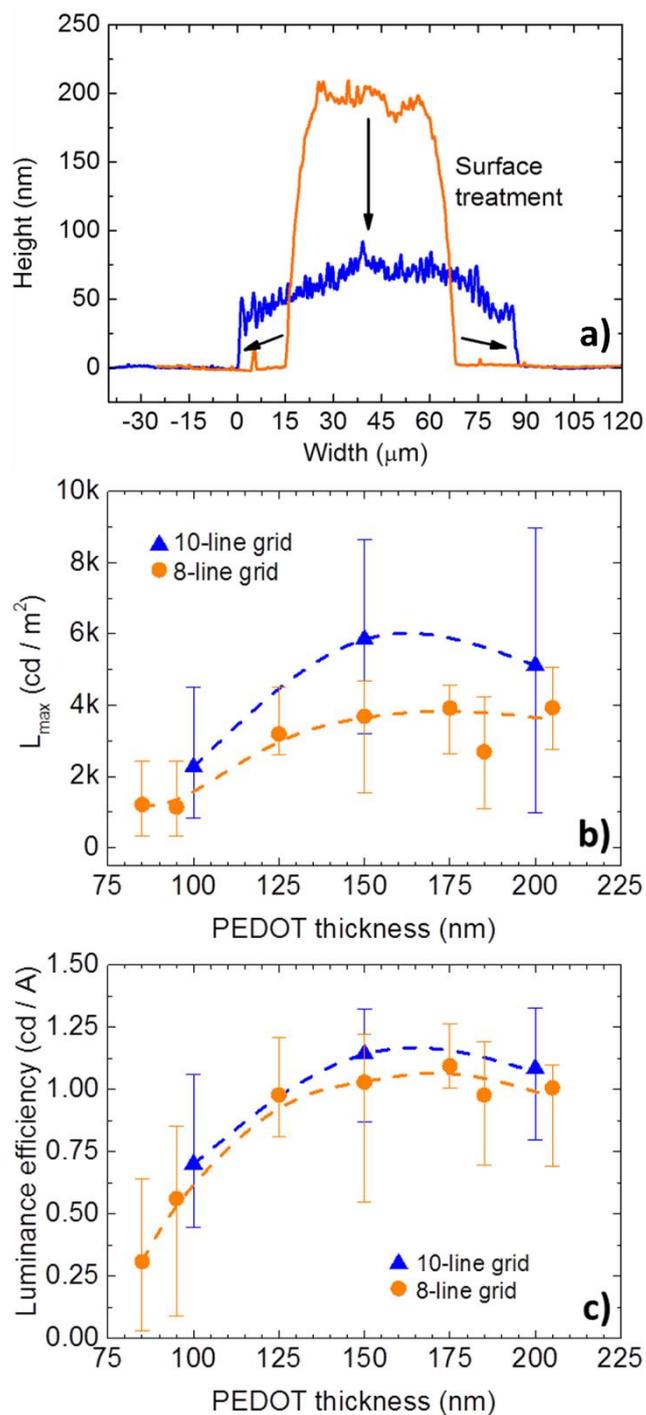


Figure 7: a) Following UV-O<sub>3</sub> surface treatment, the printed line height is reduced dramatically, b), c) the grid containing 10 printed lines achieves superior luminance and luminous efficiency performance for all PEDOT thicknesses examined.<sup>[15]</sup> – Reproduced and modified by permission of IOP Publishing.

As a result of this treatment, the height and width of the resulting grid line was 70-80 nm and ~90  $\mu\text{m}$ , respectively, therefore allowing the grid to be coated with a thinner layer of PEDOT:PSS. However, the treatment time must be carefully controlled, as too long UV-O<sub>3</sub>

treatment yields even thinner but wider lines, due to greater spreading of the ink. If the lines are too wide, transmittance is compromised.

As crucially as confirming the optimum grid line parameters, the optimum PEDOT:PSS thickness with which to coat these grid lines needed to be examined. Due to the low line height of approximately 70-80 nm, a thin PEDOT:PSS layer was targeted to cover these lines. As shown in Figure 7, PEDOT:PSS thicknesses between 85 nm and 205 nm were investigated on both 8 and 10-line grid architectures. When using thicker PEDOT:PSS films (125-175 nm), higher maximum luminance values were achieved, indicating that the combination of silver grid and PEDOT:PSS provides the best hole-injecting properties, while maintaining enough transmittance and fully covering the grids.

For all PEDOT:PSS thicknesses investigated, the 10-line grid architecture produces higher luminance and luminous efficiency, with a PEDOT:PSS layer of 150 nm thickness providing the most favourable grid/PEDOT:PSS combination. This indicates improved charge injection from the Ag grid into the PEDOT:PSS layer, which would result in a more favorable charge distribution across the entire anode.

An additional effect of the improved injection properties is the lower voltage at which peak luminous efficiency (LE) occurred in the device based on the 10-line grid, when compared to the ITO-based devices, e. g. at 4 V, LE is 1.25 cd/A for the 10-line grid compared to 1.18 cd/A for ITO. Overall, at low to medium brightness the performance parameters showed similar device efficiencies, while at higher luminance values, the ITO-based devices performed slightly better (data not shown here).

In order to understand these performance differences between grid-based and ITO-based OLEDs, the complete devices were analyzed using photocurrent mapping,<sup>[97]</sup> which provides information on the current distribution within the device. Local defects that are visualized using this technique may be an indication of diminished performance.

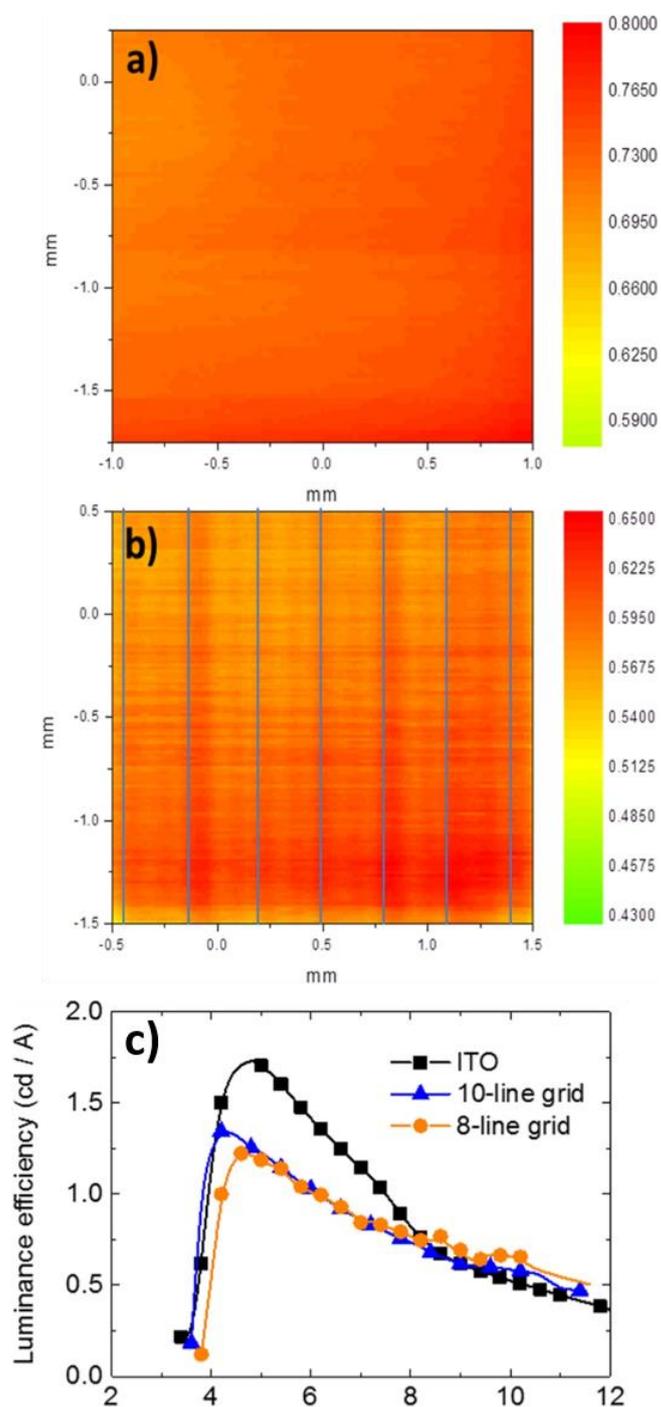


Figure 8: The photocurrent map of a 4 mm<sup>2</sup> OLED based on ITO (a) shows slightly more homogenous current distribution than the 10-line silver nanoparticle grid (b). Note that the nominal grid position is superimposed on the image. c) Comparable luminous efficiency can be seen for OLEDs based on ITO and ITO-free silver grid electrodes.<sup>[15]</sup> – Reproduced and modified by permission of IOP Publishing.

As can be seen in Figure 8, the ITO-based OLED yielded a current distribution with no visible interruptions overall and an almost uniform current of  $\sim 0.7 \mu\text{A}$  produced throughout the entire device. The Ag grid-based OLED yielded lower currents which showed slight variations

(0.55-0.65  $\mu\text{A}$ ), but the current distribution was also rather uniform, indicating a well-functioning device overall. The obtained results prove that the implemented Ag grid/PEDOT:PSS structure is a viable alternative to ITO to produce OLEDs with similar performance parameters.<sup>[15]</sup>

### 3.2 Embedding the inkjet-printed grid

In order to improve OLED performance based on these types of grid architectures further, an extremely flat and smooth electrode surface must be achieved. This will yield more homogeneous electric fields, improved injection of charge carriers, and result in more homogenous light emission. A useful method to achieve this is to embed the electrodes within a resin. This kind of embedding process for inkjet-printed electrodes was first proposed and developed for use in OPVs.<sup>[75]</sup> By building on this approach, Kinner *et al.* reported the first OLEDs based on embedded and non-embedded inkjet-printed metal-grid/PEDOT:PSS electrodes.<sup>[49]</sup>

In this case, the Ag grid was inkjet-printed in a honeycomb layout in order to utilize the advantages described in section 2.2 above. The honeycomb structure provides a balance between reducing the surface coverage (and thereby reducing shadowing) as well as providing a sufficient and homogeneously distributed number of conductive pathways, even in the case of any local line breakages after printing. The embedding was carried out using a commercially available resin (Ormocer<sup>®</sup>). With this approach any defect or roughness in the printed structure can be overcome.

In fact, the inkjet-printed grid lines were not only embedded within the resin but also transferred from the sacrificial to the final substrate by being flipped over, resulting in upside-down lines after printing (see Figure 9). The entire electrode configuration was thus planarized and provides a completely flat surface compared to non-embedded lines, which

may have small imperfections. Additionally, the transfer process allows the ink to be printed on virtually any substrate before being transferred to virtually any other substrate.

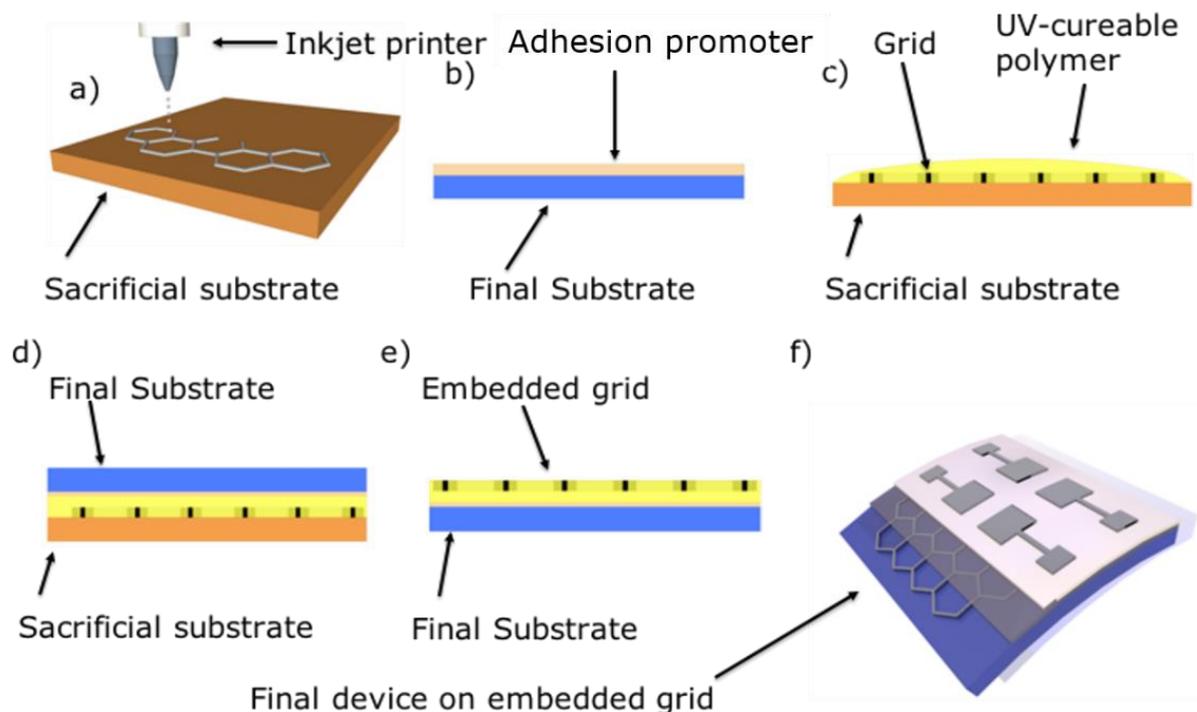


Figure 9: The reverse nano imprinting transfer results in embedded Ag grid electrodes. a) The desired electrode pattern is printed onto a sacrificial substrate, b) an adhesion promoter (Ormoprime08) is coated on the final substrate, c) a UV curable resin is coated onto the sacrificial substrate/Ag grid structure, d) the two substrates are brought together and exposed to pressure and UV light, (e) removal of the sacrificial substrate yields the desired Ag grid structure embedded within the resin. Process based on Ref.<sup>[75]</sup>

The OLED stack with inkjet-printed non-embedded and embedded Ag-lines is schematically shown in Figure 10. Before embedding, any imperfections such as spikes in the surface topography can cause shorts or parasitic currents through the layers. After embedding, the grid lines have been flattened and planarized by the nano imprinting transfer and a thin layer of PEDOT:PSS can be used to overcoat any remaining surface imperfections, thereby further reducing the risk of shorts and leakage currents through the complete device.

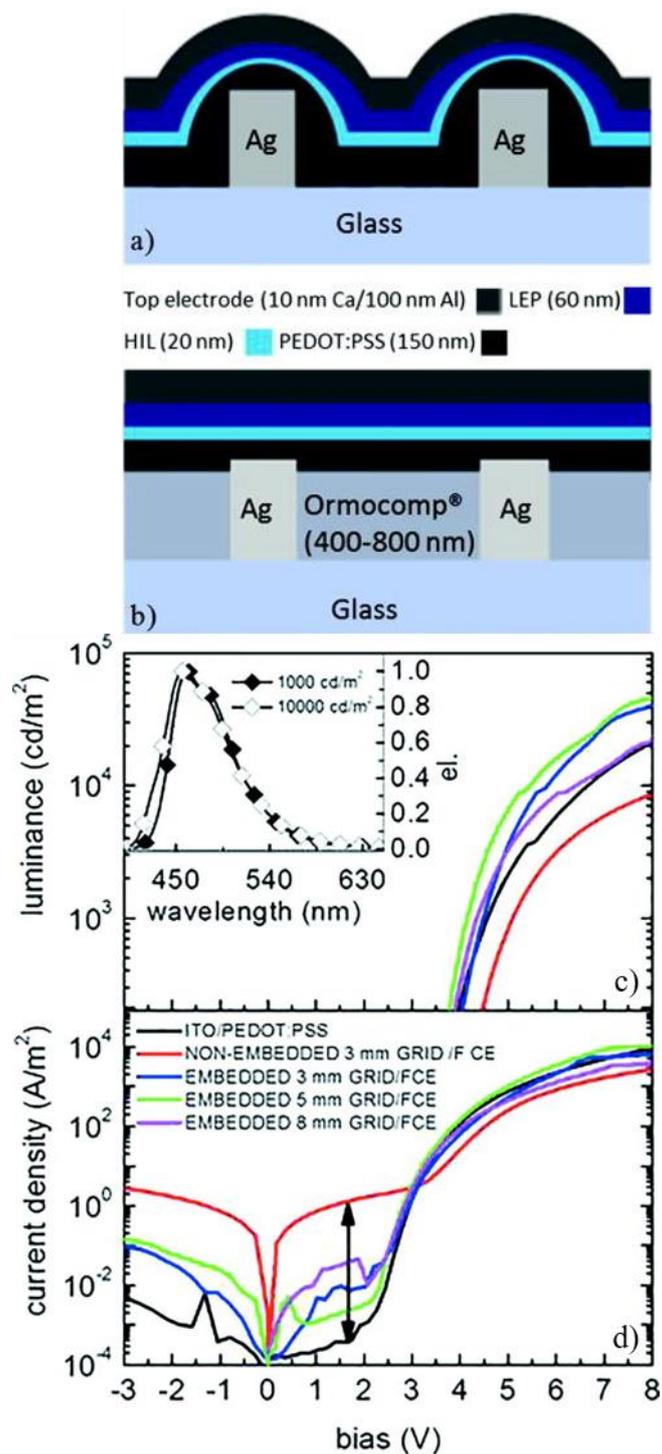


Figure 10: (a, b) Following the embedding process, the grid lines can not only be coated with a much thinner layer of PEDOT:PSS, but the risk of shunts due to penetrating metal structure is vastly reduced. The resulting device performance shows maximum luminance values between 9000 and 42000  $\text{cd/m}^2$  (c), while embedding reduces the leakage current by orders of magnitude (d).<sup>[49]</sup> – Reproduced and modified by permission of the American Institute of Physics.

Additionally, a thinner PEDOT:PSS films provides greater transparency and the optical transmission was evaluated by honeycombs with different spacing diameters. Since these type of printed grid electrodes have some areas with 100% transmission where there is no grid line and 0% transmission where there are grid lines, these measurements should be interpreted as the average transmission at each grid spacing. The final inkjet-printed Ag grids (8 mm spacing diameter, 350  $\mu\text{m}$  line width) showed comparable transmission in the relevant optical range to ITO (data not shown here) but yielded device efficiencies which were significantly higher than OLEDs based on ITO. The embedding process reduces prevalent leakage currents and, more importantly, significantly improves light outcoupling, an effect which will be discussed in detail in section 5 below.

The achieved luminescence characteristics of the described OLEDs are shown in Figure 10. The reference ITO-based OLED yielded a maximum luminance of 20000  $\text{cd}/\text{m}^2$ . While the OLED based on the non-embedded 3 mm grid/PEDOT:PSS electrode yielded a maximum luminance of 9000  $\text{cd}/\text{m}^2$ , OLEDs based on the embedded 3 and 5 mm grid/PEDOT:PSS electrodes yielded maximum luminance values of 40000  $\text{cd}/\text{m}^2$  and 42000  $\text{cd}/\text{m}^2$ , respectively. Finally, the OLED based on the 8 mm grid/PEDOT:PSS electrode showed a maximum luminance of 20000  $\text{cd}/\text{m}^2$ . Normalized electroluminescence spectra resulting from OLEDs based on the 8 mm grid/PEDOT:PSS electrode are shown in the inset of Figure 10. These confirm the stability of the emission of the light-emitting polymer at two different luminance values and are similar to the spectra obtained for OLEDs based on the reference ITO/PEDOT:PSS electrode (data not shown here).

In addition to the luminance values, Figure 10 also shows the current densities obtained from the measured OLEDs. The most prominent feature is shown by the black arrow, which indicates the difference in leakage current of almost 4 orders of magnitude between the devices based on the non-embedded grid/PEDOT:PSS electrodes and the other electrode types. Importantly however, after the grid electrodes are embedded, the resulting OLEDs

show leakage currents 400 times lower than those based on non-embedded grids. While this is still one order of magnitude higher than the ITO-based reference (which overall show the lowest leakage currents) this was largely expected since ITO is a much flatter electrode system than even the embedded grids and hence provides the lowest possibility of shorts through the device. Of the grid-based devices, the OLED containing 8 nm embedded grid/PEDOT:PSS electrodes showed the lowest values in terms of leakage current and was the highest performing device overall.<sup>[49]</sup>

#### **4 Transferring from Ag to Cu metal nanoparticle inks**

When looking at the findings detailed in the above sections, the results obtained by the various groups worldwide has shown that using silver nanoparticle ink in a current collecting or transporting grid has been thoroughly investigated and understood. Several reports have provided evidence of improved grid functionality when moving from simple straight-line to more complex geometry. This is undoubtedly as a result of an improved interface between the grid itself and the next layer processed on top of it. Through the continuous improvement of this interface and the understanding of specific interface effects such as those that can be used to couple light out of the device more efficiently (see section 5) even greater device performance can be achieved in printed organic optoelectronic devices employing Ag grids. However, while Ag has many of the benefits discussed in the above sections, its disadvantages are its high costs and its susceptibility to electromigration.<sup>[98]</sup> Overcoming these disadvantages is the reason for targeting Cu as the basis to forming low cost inks to use in current collecting grids, with its lower raw material price and high bulk conductivity (Cu:  $5.96 \times 10^5 \text{ S cm}^{-1}$ , Ag:  $6.30 \times 10^5 \text{ S cm}^{-1}$ , at 20 °C).<sup>[99-100]</sup> As with Ag, the developed inks must be suitable for large area processing methods such as R2R and additive deposition methods like inkjet printing.

However, Cu suffers from high chemical reactivity and due to the poor electrical conductivity of oxidized Cu films, it is important to prevent oxidation during the sintering of the Cu ink. One such method is by utilizing formic acid vapor,<sup>[101]</sup> or by applying a reducing agent directly into an inkjet mixture or just before the printing process.<sup>[102-105]</sup> The printed circuit board and semiconductor industry already implements formic acid in its manufacturing processes.<sup>[106-107]</sup>

#### 4.1 Organic photovoltaics based on Cu metal grids

Georgiou *et al.* presented the first optoelectronic devices based on inkjet-printed Cu grids as a replacement for ITO.<sup>[18]</sup> The solution-processed Cu nanoparticles produced high quality Cu grids with good adhesion to the substrate and the required conductivity. ITO-free OPVs based on these inkjet-printed Cu grids in combination with PEDOT:PSS were produced using the organic polymer/fullerene active layer blend of poly[2,6-(4,4-bis-(2-ethylhexyl)dithieno[3,2-b:2',3'-d]silole)-alt-4,7-(2,1,3-benzothiadiazole)]/[6,6]-phenyl C<sub>71</sub> butyric acid methyl ester (Si-PCPDTBT)/PC[70]BM) as active layer material. A power conversion efficiency (PCE) of 3.35% was achieved, while ITO-based OPVs reached a PCE of 4.92%.

The lower PCE was primarily due to oxidation of the deposited Cu grid and the reduced conductivity arising from the required annealing step of PEDOT:PSS performed in ambient conditions. Initial trials on embedding the inkjet-printed Cu grids using the same reverse nanoimprinting transfer procedure described above produced Si-PCPDTBT/PC[70]BM OPVs with a PCE of 2.56%.

As before in the case of the development process for the Ag grid-based electrode, the entirety of the electrode needed to be re-examined. This resulted in a variety of Cu grid electrodes being fabricated and tested – with a pitch of 0.6 mm (4 lines), 0.43 mm (6 lines) and 0.33 mm (8 lines). Implementing these different grids into complete OPV devices yielded an improvement of the fill factor (FF) in those OPVs containing a greater number of Cu grid lines. The short circuit

current ( $J_{sc}$ ) also increased with an increasing number of lines, however, for the highest number of lines the losses due to shadowing became more dominant.

As a result, the optimum balance between carrier collection and shadowing losses was achieved for OPVs employing a 6-line grid, giving a  $J_{sc}$  of  $11.39 \text{ mA}\cdot\text{cm}^{-2}$  and FF of 50.72%. The reference OPV device based on ITO showed a higher  $J_{sc}$  of  $14.12 \text{ mA}\cdot\text{cm}^{-2}$  and FF of 57.84%, and the majority of these losses in  $J_{sc}$  were due to shadowing effects of the Cu grid design and the lower transmission resulting from needing a thicker PEDOT:PSS layer to overcoat the grid lines.

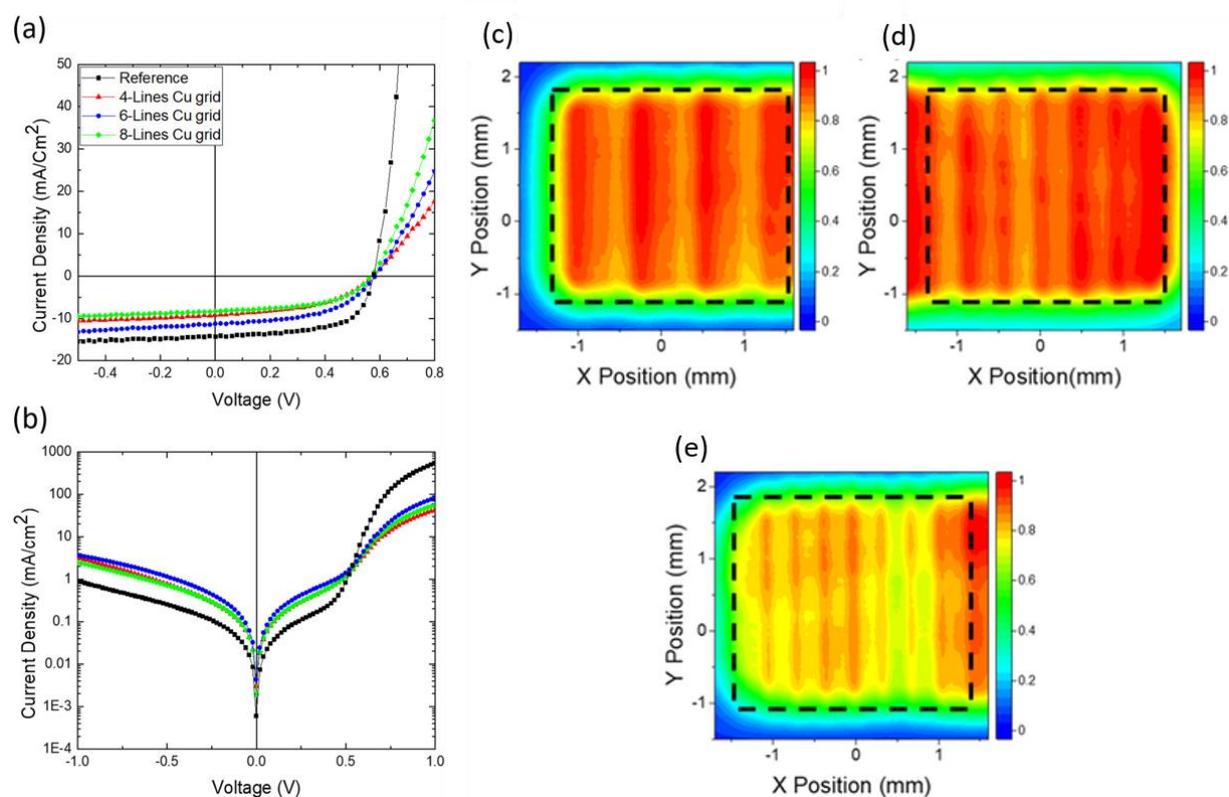


Figure 11: The J/V curves and photocurrent mapping show significantly higher series resistance for the ITO-free OPVs, resulting from losses in electrode conductivity. The photocurrent maps confirm the most homogenous current distribution using the 6-line grid architecture.<sup>[18]</sup> – Reproduced and modified by permission of Wiley VCH.

In addition to the performance parameters obtained under illuminated conditions, dark current density/voltage (J/V) curves were recorded. These provide further information on the potential loss regimes in device performance (see Figure 11). The data show a much higher series

resistance ( $R_s$ ) of the Cu grid-based OPVs than those based on ITO, owing to the low current density at forward bias.  $R_s$  is an indicator for the resistance the current experiences in the vertical direction between the layers and interfaces of the OPV device, meaning that a large factor influencing  $R_s$  is the conductivity of the electrode,<sup>[108-109]</sup> since all other stack layers are identical. One possible reason for a lower conductivity of the inkjet-printed Cu grids could be due to breakages along the printed lines.

At the same time, the Cu grid-based OPVs show a higher leakage current (i.e. a lower parallel resistance,  $R_p$ ) than ITO-based OPVs, which was similarly observed by Kinner *et al.*<sup>[49]</sup> This can be an indicator for unfavorable morphology of the printed Cu grid lines, i.e. some spikes or other imperfections may not have been fully overcoated by PEDOT:PSS, while the ITO again would have a flat and homogenous morphology. As a result of both the increased  $R_s$  and the reduced  $R_p$  values, Cu grid-based OPVs show lower overall FF than the reference ITO-based OPVs.

As for the Ag grid-based devices, photocurrent mapping measurements were carried out (Figure 11),<sup>[97]</sup> in order to determine possible defects in photocurrent distribution across the Cu grid structures within the complete OPV devices. The current distribution was efficient and homogeneous for the 4-line and 6-line Cu grid configuration, but lower and less homogeneous for the 8-line Cu grid, which was confirmed by the  $J_{sc}$  values. Overall, therefore, the 6-line grid architecture yielded the optimum OPV performance in this system.<sup>[18]</sup>

#### 4.2 Overcoming the oxidation limitation of Cu by sintering

As described above, Cu requires a post-deposition treatment process to overcome the limitations from oxidation. The OPV devices described in the previous section implement laser-sintered Cu nanoparticle grids, which can reach limitations regarding throughput and material substrate compatibility. These limitations can be overcome by a method of post deposition treatment and sintering of Cu ink using formic acid in a procedure that is

compatible with the use of low-cost flexible substrates with low glass transition temperatures.<sup>[110]</sup> The additive process is chemically facile, scalable, uses minimal material and is compatible with R2R processing, thus industrially feasible, yielding structures that have excellent adhesion and stability under manual bending tests to within 2% tensile strain. The resulting Cu films fabricated at the truly low temperature of 130 °C showed a conductivity of up to 16% bulk Cu ( $9.7 \times 10^4 \text{ S cm}^{-1}$ ) together with ISO 0 adhesion on glass as well as on the flexible substrate polyethylene terephthalate (PET). When employing sintering temperatures above 130 °C the bulk Cu conductivity increased to a remarkable 25% ( $1.6 \times 10^5 \text{ S cm}^{-1}$ ) for 150 °C and reached 31% ( $1.8 \times 10^5 \text{ S cm}^{-1}$ ) for 230 °C.

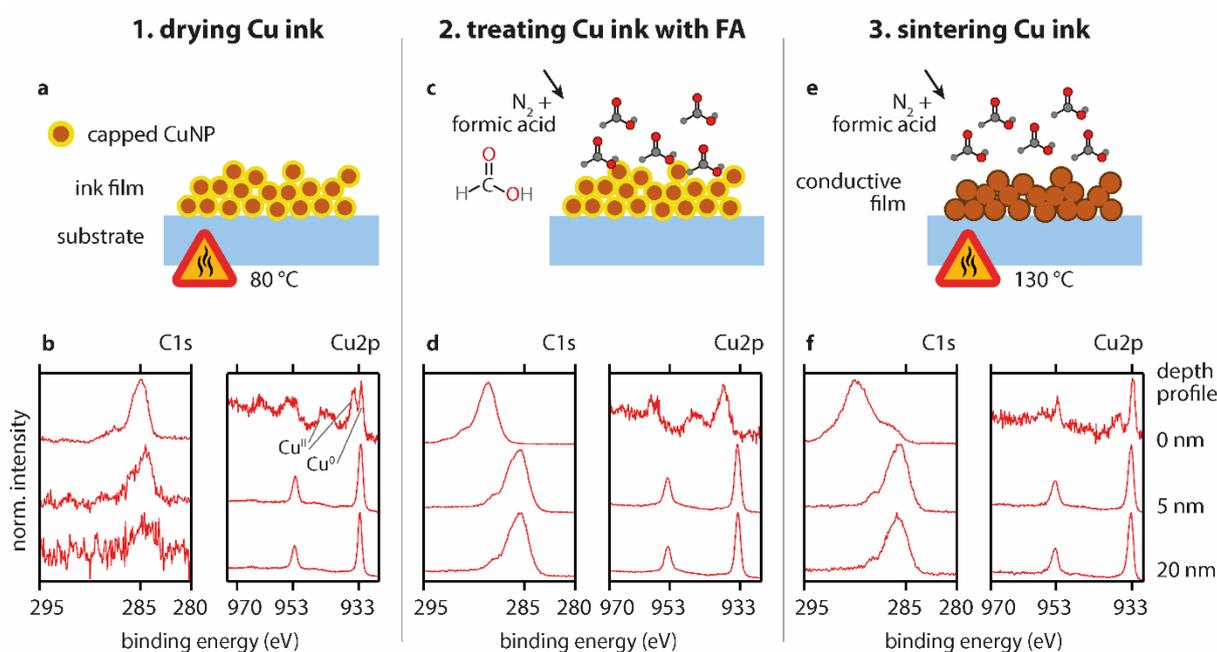


Figure 12: The dried nanoparticle film (a) is subjected to formic acid (FA) treatment (c). Upon the influence of temperature in the sintering step (e) the nanoparticles merge and form a sintered film. The developments of the C1s as well as Cu(II) and Cu(0) components of the Cu2p peak are visible in the normalised XPS spectra and corresponding depth profiles (b, d, f).<sup>[110]</sup> – Reproduced by permission of Wiley VCH.

Importantly, the 16% bulk Cu conductivity is fully suited for use as conductive features in electronic devices and is comparable to the conductivity obtained from photonically sintered Cu ink-based structures, while the achieved conductivity values for sintering temperatures at and above 150 °C were the highest reported values for conductive, thin (< 500 nm) Cu films.

The introduced process for sintering of Cu-based nanoparticle inks was as follows: After the deposition and subsequent drying at 80 °C, see Figure 12, homogenous non-conducting films were obtained. X-ray photoemission spectroscopy (XPS) studies (Figure 12b) revealed a C1s peak resulting from the organic protective ligand of the nanoparticles as well as the solvent surroundings. At the same time, the Cu2p signal shows an abundant presence of oxidized (non-conductive) Cu(II) species.

The XPS data in Figure 12 also displays the depth profile evolution of the signal achieved via sputtering. The topmost curves refer to the bare sample surface; the middle curves to a depth of approx. 5 nm, and the bottommost curves to a depth of approx. 20 nm. Through the worsening signal-to-noise ratio, the depth profile indicates consistently that the C1s signal decreases while that of the Cu2p increases. Particularly, the peak related to Cu(II) fully disappears, indicating the presence of metallic Cu.

In the next step of the treatment process, the sample films fill with formic acid after exposure to formic acid vapor at room temperature. In this step the protective ligand used on the nanoparticles is replaced by formates, as Cu surfaces show chemisorption of formic acid at room temperature.<sup>[111]</sup> The process continues into the bulk film, resulting in a swelling and increase in film thickness. This is confirmed by the C1s signal in the depth profile XPS analysis, which displays (Figure 12, left panel, topmost spectra) the typical signals of the ligand also at the layers 5 and 20 nm below the surface. The film is still non-conductive at this stage, however, confirmed by the XPS signals of this bare substrate as the spectra show a shift of the peaks towards higher binding energy as a consequence of charging effects.<sup>[112]</sup>

Upon the final sintering step (see schematic Figure 12), under application of the desired temperature and still in the presence of formic acid, desorption of the formates as well as evaporation of the solvent occurs, resulting in an overall reduction of organic compounds in the final films. As a consequence, the corresponding C1s peak in the depth profile XPS decreases (seen by the worsening signal-to-noise ratio, see Figure 12) and the Cu2p now

shows a decreased Cu(II) component but increased ratio of Cu(0) – confirmed by the fact the film is now conductive.

The final conductivity is a function of temperature, which can be attributed to the increased formate desorption and solvent evaporation from the CuNPs. The presence of formic acid is mandatory to avoid Cu oxidation and achieve conductive films. Crucially for the low temperature-compatible sintering, two processes begin to occur from 130 °C onwards: Firstly, formate decomposition and reduction of the surface Cu takes place on the Cu NP surfaces.<sup>[111, 113]</sup> Secondly, the reduced CuNPs then initiate the sintering process and form conductive films, yielding an abrupt increase in conductivity noticeable at 130 °C.

When carrying out the final sintering step at 150 °C, 26% of bulk Cu conductivity was achieved on PET; with 25% bulk Cu conductivity achieved on the comparison glass substrate. At even higher temperatures the solvent evaporation and formate desorption are even more efficient, which is apparent following sintering at 230 °C on glass, whereby the high bulk Cu conductivity of 31% was reached.<sup>[110]</sup>

## 5 Outlook

In the previous sections, the progress of implementing an overcoated Ag and embedded Ag grid conducting electrode has been elucidated. Then the progress of developing a Cu grid that has been implemented in optoelectronic devices and is compatible with low temperature sintering procedures was introduced. In this final section, an additional effect that plays a part in the transparent conducting electrode configuration of OLEDs in particular will be examined: the outcoupling of light. Optical out-coupling is an important factor of the external quantum efficiency ( $\eta_{ext}$ ) of an OLED, as it is equal to internal quantum efficiency ( $\eta_{int}$ ) multiplied by the fraction of out-coupled light ( $\eta_c$ ).<sup>[114]</sup>

When remembering the device performance discussed above in the findings of Kinner et al,<sup>[49]</sup> it is first of all necessary to compare the device performance of the produced OLEDs

containing the ITO-based reference, 3 mm non-embedded and 3 mm, 5 mm, 8 mm embedded grid/PEDOT:PSS electrodes by plotting the device efficiency against the luminance (see Figure 13).

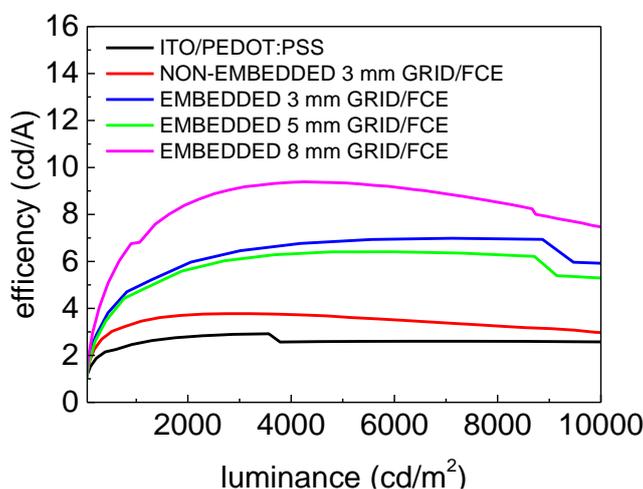


Figure 13: Current efficiency is vastly improved when moving from the ITO-based to non-embedded and then embedded grid architecture.<sup>[49]</sup> – Reproduced by permission of the American Institute of Physics.

The OLED based on ITO has the lowest overall maximum device efficiency with 3.4 cd/A.

Using the non-embedded 3 mm grid/PEDOT:PSS electrode, the OLED efficiency increases to a maximum of 3.7 cd/A below a luminance of 10000 cd/m<sup>2</sup>. At the same time, the current density remains similar, in other words, more light is able to escape the device resulting in a higher current efficiency. At a higher current density of 6500 A/m<sup>2</sup>, the ITO-based device shows a higher luminance of 13 000 cd/m<sup>2</sup> versus the 12 000 cd/m<sup>2</sup> for the non-embedded grid-based device (Figure 10).

This can be explained by an improved light out-coupling through the interface of PEDOT:PSS/glass compared to the interface of PEDOT:PSS/ITO/glass,<sup>[115-117]</sup> especially at lower luminance. As the photons generated within the active layer of the ITO-based OLEDs encounter each interface on their way out of the device, 60-80% of this light is trapped because of the worse refractive index matching of the layer of ITO ( $n=2.10$  at 450 nm) in between the organic materials ( $n\approx 1.75$ ) and glass ( $n=1.45$ ).<sup>[118]</sup>

Using the transfer matrix method, light outcoupling was calculated for normal incident light at 450 nm, without inclusion of the identical Ca/Al top contact. Applying the method yielded a transmittance of 80% for the ITO-based device stack, while the non-embedded grid-based device had a transmittance of 99% (grid lines are not accounted for). It is reasonable to assume both devices have a similar  $\eta_{int}$  due to the identical cathode layer mentioned above, as well as the same light emitting layer and operation at comparable current densities. An improved efficiency ( $\eta_{ext}$ ) (shadowing and electrical losses are not accounted for) of 22% for light outcoupling.

Moving to embedding, the OLEDs built on the 3 mm and 5 mm embedded grid/PEDOT:PSS electrodes yield device efficiencies of up to 7 and 6.4 cd/A, respectively. This efficiency increase when moving from non-embedding to embedding is not only due to the improvement in reduction of the leakage currents. Since the OLEDs incorporating the embedded 3 mm grid show a significantly higher luminance of 40000 cd/m<sup>2</sup> at 6500 A/m<sup>2</sup> than the 12000 cd/m<sup>2</sup> of the non-embedded 3 mm grid device, this indicates a further improvement in light outcoupling from the device.

This is due to the improved refractive index matching when the layer of ITO ( $n=2.10$ ) is replaced with the resin Ormocomp<sup>®</sup> ( $n=1.52$ ), into which the grids are embedded, thereby allowing more light to be extracted from the device.<sup>[119]</sup> (It must be noted that in some parts of the grid the light is completely blocked from escaping due to the presence of the grid line itself, so that this is an average effect across the whole transparent anode). Finally, OLEDs incorporating the 8 mm grid/PEDOT:PSS electrode exhibit the highest efficiency of all investigated devices of up to 9.4 cd/A. The grid geometry has been improved by minimizing the remaining grid lines across the active area and as a result any remaining shunt losses are reduced, while also significantly reducing light blocking.

The explanation for this final improvement in device efficiency needs to take into account the light that couples into the substrate as well as into waveguide modes. Typically, the

percentage of the light generated within the OLED which couples into the combination of waveguide, substrate, plasmonic as well as other electrical loss modes is within the same order of magnitude or higher than the percentage of outcoupled light.<sup>[120-122]</sup>

This means that if light is not lost into these loss channels and due to the calculated out-coupling factor  $\eta_c(\lambda)$  being close to 99%, light otherwise lost into waveguide and substrate modes will be coupled out of the device and yield the observed overall increase in device efficiency.<sup>[49]</sup>

Besides these outcoupling considerations, the printing of nanoparticle grids can be combined with imprinting methods to access plasmonic effects. For example, plasmonic nanoparticle arrays which feature collective lattice resonances (CLRs), i.e., plasmon resonance-induced grating effects,<sup>[123-124]</sup> can allow a high level of control over light outcoupling.

By employing Al nanoparticle arrays embedded within the color conversion layer of solid-state lighting devices, light emission has been improved as a result of enhanced light generation and outcoupling.<sup>[125]</sup> More recently, CLRs showed enhancement within light-emitting field effect transistors<sup>[126]</sup> and such arrays can also serve to tune the emission color of an OLED. This is achieved by balancing the ohmic damping of the localized surface plasmon resonance and the CLRs through the choice of the lattice constant.<sup>[127]</sup>

By selecting the appropriate lattice constant of an Al nanodisc array with square shaped symmetry and embedding it within a phosphorescent blue OLED configuration, device performance is improved by increasing the current efficiency by 35%.<sup>[128]</sup> At the same time, an imprinted grating can be used to enhance the absorption over a broad wavelength range.<sup>[129]</sup>

It is clear that by combining printing with imprinting techniques such as nanoimprint lithography, plasmonic effects can be utilized to enhance light management, i.e. incoupling of light into an OPV and outcoupling of light from an OLED, to further enhance optoelectronic device performance.

By gaining ultimate control of all the improvements in incoupling and outcoupling of light for these types of optoelectronic devices, future applications can be envisioned that additionally take advantage of the material flexibility which solution-processing provides. For example, wearable or stretchable electronics can be targeted by integrating OLEDs or OPVs into textiles, thereby advancing the wearable electronics industry.

## 6 Conclusions

This progress report has highlighted the implementation of printed nanoparticle based ITO-free transparent conductive electrode in a series of optoelectronic devices. Starting from optimising the Ag nanoparticle based bottom electrode properties of thickness and height and being able to apply a simple overcoat procedure to yield comparable performance to ITO-based OLED devices, the Ag-grid architecture was embedded to yield highly efficient OLED structures with superior device performance to those based on ITO.

Moving to printed Cu nanoparticles instead of Ag in order to address financial and resource-based considerations, the device efficiency in OPVs was shown to be slightly lower than those based on ITO, however, the devices were fully produced in ambient air conditions.

Furthermore, by optimising the sintering procedure required to produce conducting metal structures based on Cu metal nanoparticle inks, and by addressing its compatibility with low-temperature stable substrates, highly conductive Cu films could be produced, which will pave the way for implementation in fully functioning printed optoelectronic devices.

Finally, the important factor of light outcoupling was addressed in OLEDs by firstly utilising the refractive index of the chosen resin in which the transparent conductive electrode is embedded to influence the outcoupling efficiency of light produced within the active layer.

Secondly, by combining printing of nanoparticles with lithographically imprinted structures, plasmonic effects can be utilized to selectively enhance absorption as well as electroluminescence. All these approaches show the importance of continuing work in the

printed conductive electrode for use in organic and hybrid optoelectronic devices in order to further improve device performance.

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**This progress report highlights the development process of implementing inkjet-printed transparent conductive electrodes in optoelectronic devices. Firstly, the process of overcoating and embedding Ag nanoparticle-based grids in OLEDs is introduced. Secondly, Cu nanoparticle-based grids are implemented in OPVs and examined with regard to their compatibility to low temperature sintering procedures. In an outlook, the important aspect of light management in optoelectronic devices is discussed with regard to refractive index control and plasmonic effects.**

**Keywords:** solution-processed optoelectronics, inkjet-printed electronics, metal nanoparticle grid transparent electrode, embedded silver and copper electrode

Felix Hermerschmidt,\* Stelios A. Choulis, Emil J. W. List-Kratochvil\*

**A progress report on implementing inkjet-printed transparent conductive electrodes in solution-processed organic electronics**

## Supporting Information

### Progress Report

*Felix Hermerschmidt,\* Stelios A. Choulis, Emil J. W. List-Kratochvil\**

#### Short CVs

Felix Hermerschmidt received his PhD in materials science and engineering from Cyprus University of Technology, focusing on inkjet-printed active layers and electrodes in organic solar cells and OLEDs under the supervision of Prof. Stelios A. Choulis. Before that, having obtained his BSc in chemistry at Freie Universität Berlin, he completed an MRes in green chemistry at Imperial College London, focusing on morphology control in solution-processed solar cell active layers under the supervision of Profs. Natalie Stingelin and James R. Durrant. He is currently post-doctoral researcher in Prof. Emil List-Kratochvil's Hybrid Devices group at Humboldt-Universität zu Berlin with expertise in the formulation, processing and characterisation of functional materials in hybrid optoelectronic devices as well as in printed electronics.

Stelios A. Choulis is Professor of Material Science and Engineering at the Cyprus University of Technology (2008-present). He was the Organic Photovoltaic Device group leader of Konarka Technologies (2006-2008) and research and development (R&D) engineer of the Osram Opto-Semiconductors Inc. Organic Light Emitting Diode R&D team (2004-2006). During his PhD and first post-doc research associate (PDRA) position at the Advanced Technology Institute (1999-2002, University of Surrey) he investigated the optical properties of quantum electronic materials and opto-electronic devices. In 2002 he joined the center of electronic materials and devices (Imperial College London, UK) as PDRA and worked on the transport and recombination dynamics properties of molecular semiconductor materials and devices (2002-2004). His current research interest focuses on the development of functional materials and novel electrodes for solution processed optoelectronic applications.

Emil List-Kratochvil graduated in technical physics (1998) and received a doctoral degree in experimental solid-state physics (2000) from Graz University of Technology, where he also went on to complete his habilitation in solid-state physics (2003) to become Associate Professor (2004). Between 2006 and 2015 he was appointed Scientific Managing Director of the NTC Weiz GmbH. Since 2015 he is full Professor for Hybrid Devices in a joint appointment in the departments of physics and chemistry of Humboldt-Universität zu Berlin. He is an expert in (printed) semiconductor devices and structure to property relationships in organic and hybrid semiconductors. With his team he is currently working on electronic and optoelectronic components (based on hybrid material systems and organic or hybrid semiconductors), additive resource-efficient deposition techniques and in-situ nanostructuring and synthesis methods. Since 2018 he is also leader of the joint research group Generative Production Processes for Hybrid Components at Helmholtz-Zentrum Berlin.